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**Data Sheet**

August 15, 2026

## JTAG TECHNOLOGIES

# JTAG Technologies JT3707/TSI DataBlaster Boundary-Scan Controller

The JTAG Technologies JT3707/TSI DataBlaster is a boundary-scan controller for IEEE 1149.1 testing and in-system programming (ISP). It delivers sustained test clock speeds to 40 MHz with programmable TCK adjustment from 500 Hz to maximum, enabling flexible performance tuning for complex, high-density PCBs. The triple-serial interface (TSI) provides USB 2.0 (480 Mbit/s), USB 1.1 (12 Mbit/s), Ethernet, and FireWire (IEEE-1394-1995 and IEEE-1394A-2000) connectivity at 100, 200, and 400 Mbit/s operation, supporting integration across diverse computing platforms. FireWire (IEEE-1394-1995 and IEEE-1394A-2000) at 100, 200, and 400 Mbit/s TAP Architecture: Four synchronized test access ports via included JT 2147 QuadPOD system; each TAP pod (JT 2149) features a 20-way TAP connector and eight-pin connector for four static I/O lines TAP Voltage Range: 1.0 V to 3.6 V, individually programmable per



## MODEL

**JTAG  
Technologies  
JT3707/TSI**

## CALIBRATION

**Available on  
request**

## CONDITION

**Used**

## STATUS

**In stock**

## RFQ / SKU

**JT3707/TSI**

## LISTING

<https://aumictechlabs.com/products/jt3707-tsi-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

**This unit**

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programmable TCK adjustment from 500 Hz to maximum, enabling flexible performance tuning for complex, high-density PCBs. The triple-serial interface (TSI) provides USB 2.0 (480 Mbit/s), USB 1.1 (12 Mbit/s), Ethernet, and FireWire (IEEE-1394-1995 and IEEE-1394A-2000) connectivity at 100, 200, and 400 Mbit/s operation, supporting integration across diverse computing platforms.

Technical Specifications Core Capability: IEEE 1149.1 boundary-scan testing and in-system flash programming Test Clock (TCK): 40 MHz sustained; programmable from 500 Hz to maximum; automatic speed matching for optimal chain performance Connectivity Interfaces: USB 2.0 at 480 Mbit/s and USB 1.1 at 12 Mbit/s Ethernet FireWire (IEEE-1394-1995 and IEEE-1394A-2000) at 100, 200, and 400 Mbit/s TAP Architecture: Four synchronized test access ports via included JT 2147 QuadPOD system; each TAP pod (JT 2149) features a 20-way TAP connector and eight-pin connector for four static I/O lines TAP Voltage Range: 1.0 V to 3.6 V, individually programmable per TAP Flash Memory Buffer: Expandable up to 128 Mbits QuadPOD System (JT 2147): Bench-top signal conditioning module comprising JT 2148 transceiver and four removable JT 2149 TAP pods, connected via 50-way shielded cable assembly Key Features Scalable architecture upgradeable to JT3717 with JT 2108 ETT (Enhanced Throughput Technology) module Optimized binary file format (BSX) for high-speed execution Optional SCIL measurement modules and JT 2149/DAF (Digital/Analog/Frequency) modules for expanded capability Full compatibility with existing JTAG Technologies test and ISP files Typical Applications Board testing, CPLD programming, fast in-system flash memory programming, vector-intensive manufacturing test, and engineering environments with small-to-large data block requirements Compatibility & Integration Fully compatible with existing JTAG Technologies test and ISP file libraries. Low-voltage TAP system supports modern embedded and FPGA device ecosystems.

## JT3707TSI Characteristics / Specifications

| PARAMETER                   | VALUE                               |
|-----------------------------|-------------------------------------|
| Model                       | JT3707/TSI                          |
| Instrument Type             | boundary scan controller            |
| Number Of Taps              | 4                                   |
| Tck Range Internal          | 1 kHz to 40 MHz                     |
| Output Voltage Range        | 1.0 V to 3.6 V                      |
| Input Voltage Threshold     | 0.6 V to 1.8 V                      |
| Digital Io Count            | 16 total 4 per TAP                  |
| Internal Fifo Buffer        | 0 Mbits                             |
| Host Interfaces             | USB Ethernet FireWire               |
| Main Unit Dimensions Mm     | 135 × 220 × 40                      |
| Quadpod Dimensions Mm       | 190 × 127 × 24                      |
| Main Unit Mass              | 0.73 kg                             |
| Quadpod Mass                | 0.35 kg                             |
| Power Supply                | 12 V 2 A center negative jack       |
| Pod Cable                   | high density 50 way shielded        |
| Standard Tap Pod            | JT 2149 20 way connector            |
| Static Io Per Pod           | 4 lines on 8 pin connector          |
| Execution Format            | BSX optimized binary                |
| Series Family               | JT 37×7 DataBlaster                 |
| Variant Role                | base level engineering model        |
| Operating Temperature Class | 0 C to 40 C lab                     |
| Tap Extender Option         | 1 m extender without TCK loss class |
| Manufacturer                | JTAG Technologies                   |
| Operating temperature       | Class: 0 C to 40 C lab              |

## JT3707TSI Specifications

| PARAMETER                            | VALUE                                                                                                                   |
|--------------------------------------|-------------------------------------------------------------------------------------------------------------------------|
| Applications                         | Board structural test, CPLD programming, and small-block flash programming in engineering and low-volume manufacturing. |
| Key Features                         | 40 MHz maximum TCK                                                                                                      |
| USB Ethernet and FireWire host links | Supplied JT 2147 QuadPod with JT 2149 TAP pods                                                                          |
| Technical Specifications             | Model: JT3707/TSI                                                                                                       |
| Notes                                | From JTAG Technologies JT 37×7/TSI datasheet 2112-JT37×7-TSI-E-01 specification table for JT 3707/TSI.                  |

# General Specifications & Supplementary Characteristics

| PARAMETER                   | VALUE                                                                                                                             |
|-----------------------------|-----------------------------------------------------------------------------------------------------------------------------------|
| Operating temperature       | Class: 0 C to 40 C lab                                                                                                            |
| Operating Temperature       | Class: 0 C to 40 C lab                                                                                                            |
| Operating Temperature Class | 0 C to 40 C lab                                                                                                                   |
| Tap Extender Option         | 1 m extender without TCK loss class                                                                                               |
| Manufacturer                | JTAG Technologies Notes From JTAG Technologies JT 37×7/TSI<br>datasheet 2112-JT37×7-TSI-E-01 specification table for JT 3707/TSI. |

# Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

| Specifications                                                                                         |                                     |
|--------------------------------------------------------------------------------------------------------|-------------------------------------|
| Specifications                                                                                         |                                     |
| PARAMETER                                                                                              | VALUE                               |
| Model                                                                                                  | JT3707/TSI                          |
| Instrument Type                                                                                        | boundary scan controller            |
| Number Of Taps                                                                                         | 4                                   |
| Tck Range Internal                                                                                     | 1 kHz to 40 MHz                     |
| Output Voltage Range                                                                                   | 1.0 V to 3.6 V                      |
| Input Voltage Threshold                                                                                | 0.6 V to 1.8 V                      |
| Digital Io Count                                                                                       | 16 total 4 per TAP                  |
| Internal Fifo Buffer                                                                                   | 0 Mbits                             |
| Host Interfaces                                                                                        | USB Ethernet FireWire               |
| Main Unit Dimensions Mm                                                                                | 135 x 220 x 40                      |
| Quadpod Dimensions Mm                                                                                  | 190 x 127 x 24                      |
| Main Unit Mass                                                                                         | 0.73 kg                             |
| Quadpod Mass                                                                                           | 0.35 kg                             |
| Power Supply                                                                                           | 12 V 2 A center negative jack       |
| Pod Cable                                                                                              | high density 50 way shielded        |
| Standard Tap Pod                                                                                       | JT 2149 20 way connector            |
| Static Io Per Pod                                                                                      | 4 lines on 8 pin connector          |
| Execution Format                                                                                       | BSX optimized binary                |
| Series Family                                                                                          | JT 37x7 DataBlaster                 |
| Variant Role                                                                                           | base level engineering model        |
| Operating Temperature Class                                                                            | 0 C to 40 C lab                     |
| Tap Extender Option                                                                                    | 1 m extender without TCK loss class |
| Manufacturer                                                                                           | JTAG Technologies                   |
| Notes                                                                                                  |                                     |
| From JTAG Technologies JT 37x7/TSI datasheet 2112-JT37x7-TSI-E-01 specification table for JT 3707/TSI. |                                     |

### Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: [sales@aumictech.com](mailto:sales@aumictech.com) · +1 (484) 841-9341

#### Aumictech

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NIST-traceable calibration · SAM registered ·  
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.