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Data Sheet

August 15, 2026

JTAG TECHNOLOGIES

JTAG Technologies PM3705 Explorer High-Performance Boundary-Scan Controller

JTAG Technologies JT3705/USB Explorer Boundary Scan Controller The JT 3705/USB Explorer (cataloged in some queues as PM3705 Explorer) is a portable USB-powered boundary-scan controller. OEM datasheet 2102-JT3705-USB-E-03 lists 2 synchronized TAPs, programmable TCK from 1 kHz to 6 MHz, and programmable output levels 1.5 V to 3.6 V per TAP. Low-volume board test, CPLD and small flash in-system programming, and benchtop prototype debug. Compatible with JTAG ProVision and JTAGLive The JT 3705/USB Explorer (cataloged in some queues as PM3705 Explorer) is a portable USB-powered boundary-scan controller. OEM datasheet 2102-JT3705-USB-E-03 lists 2 synchronized TAPs, programmable TCK from 1 kHz to 6 MHz, and programmable output levels 1.5 V to 3.6 V per TAP. Low-volume board test, CPLD and small flash in-system programming, and benchtop prototype debug. Compatible with JTAG ProVision and JTAGLive Instrument Type: boundary scan controller



MODEL

**JTAG
Technologies
PM3705**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

PM3705

LISTING

<https://aumictechlabs.com/products/pm3705-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The JTAG Technologies PM3705 Explorer is a boundary-scan controller engineered for structural board testing and in-system programming of programmable logic devices and flash memories. This self-contained desktop unit connects via PC parallel port and operates at 150 kHz, delivering two synchronized TAP output channels for simultaneous independent chain testing.

Technical Specifications The PM3705 Explorer targets 5V operation and maintains full compliance with IEEE 1149.1, IEEE 1149.6, IEEE 1149.4, and IEEE 1532 standards. The dual TAP channels enable parallel test execution across multiple boundary-scan chains with synchronized control for coordinated test pattern application.

Key Features Two parallel TAP output channels for simultaneous driving and observation of independent boundary-scan chains PC parallel port host interface operating at the inherent speed of 150 kHz TAP channel synchronization for coordinated multi-device testing Support for in-system programming (ISP) of (C)PLDs and flash memories Structural boundary-scan testing capability Connectorized form factor designed for portable bench-top deployment

Typical Applications The PM3705 Explorer is suited to engineering environments and low-volume production scenarios requiring board-level functional and structural test coverage. Primary use cases include PLD programming, flash memory programming, and comprehensive JTAG-based circuit validation during prototype development and small-batch manufacturing.

Compatibility & Integration The controller integrates with JTAG Technologies developer tools, including JTAG ProVision and JTAG Live software platforms. The parallel port interface and standards-compliant TAP implementation enable integration into existing JTAG test infrastructures without requiring specialized host adapters or protocol converters.

PM3705 Characteristics / Specifications

PARAMETER	VALUE
Model	JT3705/USB
Catalog Alias	PM3705 Explorer
Instrument Type	boundary scan controller
Number Of Taps	2
Tck Range	1 kHz to 6 MHz
Output Voltage Range Per Tap	1.5 V to 3.6 V
Host Interface	USB
Power Source	USB bus powered
Form Factor	lightweight pocket sized
Tap Synchronization	dual TAP synchronized tests supported
Multi Unit Linking	multiple units linkable in ProVision
Software Compatibility	JTAG ProVision JTAGLive Script Studio
Use Case	structural test and ISP of CPLD flash
Series Family	JTAG Explorer controllers
Maximum Tck Class	6 MHz
Operating Temperature Class	0 C to 40 C lab
Manufacturer	JTAG Technologies
Target Volume	low volume engineering
Tap Count Expansion	link multiple explorers
Logic Family Support	programmable levels for modern families
Portability	USB cable only power and control
Application Code Path	ProVision and JTAGLive
Operating temperature	Class: 0 C to 40 C lab

PM3705 Specifications

PARAMETER	VALUE
Applications	Low-volume board test, CPLD and small flash in-system programming, and benchtop prototype debug.
Two synchronized TAPs	6 MHz maximum TCK
Technical Specifications	Model: JT3705/USB

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Operating temperature	Class: 0 C to 40 C lab
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Manufacturer	JTAG Technologies
Target Volume	low volume engineering
Tap Count Expansion	link multiple explorers
Logic Family Support	programmable levels for modern families
Portability	USB cable only power and control
Application Code Path	ProVision and JTAGLive Notes From JTAG Technologies JT 3705/USB Explorer datasheet 2102-JT3705-USB-E-03. Queue title PM3705 Explorer maps to this USB Explorer controller.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.