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Data Sheet

August 15, 2026

JOERGER ENTERPRISES

Joerger Enterprises VTR10012 Eight Channel, 100 MHz, 12 Bit VME Analog Digitizer with Oscilloscope Characteristics

Joerger Enterprises VTR10012 Eight Channel 100 MHz 12 Bit VME Digitizer The Joerger VTR10012 is a 6U VME analog digitizer with oscilloscope-style inputs. OEM product literature lists 1 2 4 or 8 individual 100 MHz digitizer channels, 12 bit resolution with signal averaging, 256 ksamples SRAM per channel (2 Msamples total), 10 MOhm input impedance, and about 13 W power at 100 MHz. Single-shot high-speed waveform recording, multi-channel transient capture, and crate-based experimental physics digitizing. The Joerger VTR10012 is a 6U VME analog digitizer with oscilloscope-style inputs. OEM product literature lists 1 2 4 or 8 individual 100 MHz digitizer channels, 12 bit resolution with signal averaging, 256 ksamples SRAM per channel (2 Msamples total), 10 MOhm input impedance, and about 13 W power at 100 MHz. Single-shot high-speed waveform recording, multi-channel transient capture, and crate-based experimental physics digitizing. Channel Configurations: 1 2 4 or 8 channels Input Modes: single ended or differential



MODEL

**Joerger
Enterprises
VTR10012**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

VTR10012

LISTING

<https://aumictechlabs.com/products/vtr10012-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Joerger Enterprises VTR10012 is an eight-channel, 12-bit VME analog digitizer operating at 100 MHz sampling rate per channel. Designed for high-resolution, high-bandwidth data acquisition in VMEbus systems, this single-width 6U module delivers simultaneous sampling across all channels with oscilloscope-grade performance. The VTR10012 combines 2M samples total SRAM (256K per channel), low crosstalk below 1 LSB, and flexible triggering modes including Pre/Post Trigger and Multiple Post Trigger configurations. Individual channel filtering on analog ground and power lines minimizes noise, while 32-bit real-time trigger arrival timestamping with 10 ns resolution enables precise event correlation. Signal averaging from 2 to 128 samples improves SNR for demanding applications. Extended VMEbus addressing (A32) supports efficient 2-channel readout over D0-D31, with 64-bit MBLT64 capability for accelerated data transfer. The module operates at just 13 W, reducing thermal load in VME crates.

Technical Specifications

Channels: Eight simultaneous sampling channels **Sampling Rate:** 100 MHz per channel **Resolution:** 12 bits, straight offset binary format **Memory:** 256K samples per channel; 2M samples total across all channels **Input Impedance:** 10 M Ω **Input Range:** ± 1 V (single-ended); single-ended or differential inputs optional **Input Offset Adjustment:** Full-scale front panel control with test point **Signal Averaging:** 2 to 128 selectable input samples **Channel Crosstalk:** <1 LSB **Real-Time Counter:** 32-bit RTC, selectable resolution up to 10 ns **Memory Access:** Extended addressing (A32); readout when module disarmed only **Data Readout:** 2 channels per cycle; D0-D11 (channels 1-4), D16-D27 (channels 5-8); 64-bit MBLT64 available **Over Range Bit:** D12 and D28, jumper selectable **Power Consumption:** 13 W at 100 MHz **Form Factor:** Single-width, 6U VME module **Clock:** Internal crystal or external clock input

Key Features Four trigger recording modes: Post Trigger, Multiple Post Trigger, Pre/Post Trigger, Multiple Pre/Post Trigger Sparse Scan Mode with programmable per-channel or global threshold for optimized readout speed Trigger address storage with counter and event register Global commands for synchronous multi-module control Per-channel analog filtering on ground and power for noise reduction

Typical Applications High-speed waveform capture and transient analysis Multi-channel signal correlation and time-stamped event logging Physics and instrumentation test benches requiring simultaneous sampling

Compatibility & Integration VMEbus systems supporting extended addressing (A32) and D0-D31 data paths. Compatible with single or differential input configurations.

VTR10012 Characteristics / Specifications

PARAMETER	VALUE
Model	VTR10012
Instrument Type	VME analog digitizer
Form Factor	6U single width VME
Maximum Clock Rate	100 MHz
Vertical Resolution	12 bits
Channel Configurations	1 2 4 or 8 channels
Memory Per Channel	256 ksamples SRAM
Total Memory Eight Channel	2 Msamples
Signal Averaging Depth	2 to 128 samples
Input Impedance	10 MOhm
Input Modes	single ended or differential
Full Scale Offset	supported
Trigger Counter	included
Global Commands	multi module synchronized operation
Power Dissipation At 100 Mhz	13 W
Adc Architecture	pipelined converter
Use Case	single shot high resolution capture
Operating Temperature Class	0 C to 50 C lab
Bus Interface	VMEbus
Series Family	Joerger VTR100xx
Averaging Benefit	improved SNR versus single shot
Channel Independence	each channel self contained
Readout	fast VME memory readout after capture
Operating temperature	Class: 0 C to 50 C lab

VTR10012 Specifications

PARAMETER	VALUE
Key Features	Up to eight 100 MHz digitizer channels
Oscilloscope style 10 MOhm inputs	Low 13 W power at full clock rate
Technical Specifications	Model: VTR10012
Notes	Channel count, 100 MHz/12-bit, 256k SRAM, averaging 2-128, 10 MOhm inputs, and 13 W from Joerger VTR10012 OEM/product literature summaries.

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
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Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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NIST-traceable calibration · SAM registered ·
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.