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Data Sheet

August 13, 2026

KEITHLEY

Keithley CTM-10 Counter / Timer Board

Keithley CTM-10 ISA 10 Channel Counter Timer Board The Keithley MetraByte CTM-10 is a 10-channel 16-bit counter/timer ISA board using two AMD 9513 (or equivalent) chips. Companion CTM-05 provides five channels on one 9513. Typical maximum count rate is 7 MHz class. Functions include event counting, frequency measurement, rate generation, one-shot and repetitive one-shot pulse output, and programmable gates/polarities. Digital I/O on the 9513 ports. DriverLINX / DAS-family software. Ten 16-bit counters (two 9513 devices) versus five on CTM-05 The Keithley MetraByte CTM-10 is a 10-channel 16-bit counter/timer ISA board using two AMD 9513 (or equivalent) chips. Companion CTM-05 provides five channels on one 9513. Typical maximum count rate is 7 MHz class. Functions include event counting, frequency measurement, rate generation, one-shot and repetitive one-shot pulse output, and programmable gates/polarities. Digital I/O on the 9513 ports. DriverLINX / DAS-family software. Ten 16-bit counters (two 9513 devices) versus five on CTM-05 Instrument Type: ISA 10 channel counter timer board



MODEL

Keithley CTM-10

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

CTM-10

LISTING

<https://aumictechlabs.com/products/ctm-10-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Keithley CTM-10 is a counter/timer board for ISA-bus compatible PCs delivering precision timing and counting across frequency measurement, pulse width analysis, event counting, and pulse generation. Built around two Am9513A counter/timer chips, It has ten independent 16-bit

counters capable of up to 7 MHz input rates with 70 ns minimum pulse width resolution. The board integrates selectable 1 MHz or 5 MHz onboard time bases ($\pm 0.01\%$ accuracy, 0–70°C), software-configurable gating, input/output polarity control, and complex pulse generation. All ten counters link software-selectively to form 32-, 48-, 64-, or 80-bit counters. An 8-bit latched digital input port and 16-bit latched digital output port (LSTTL compatible) support AT/XT interrupt logic with software-selectable levels (3, 5, 7, 10, 11, 15) and jumper-selectable fallback options (2–7). Independent interrupt handling across the board's two 5-channel halves, combined with Load and Hold registers per counter, enables deterministic automated test integration.

Technical Specifications

Ten 16-bit counters (two Am9513A chips)

Maximum input frequency: 7 MHz

Minimum pulse width: 70 ns (high and low)

Onboard time bases: 1 MHz or 5 MHz at $\pm 0.01\%$ accuracy (0–70°C)

Counter modes: Up/down, binary or BCD

Linked counter formation: 32-, 48-, 64-, or 80-bit configurations

Load Register and Hold Register per counter

Input Low: 0.8 V max (1 mA max input current)

Input High: 2.2 V min ($-10\ \mu\text{A}$ max input current)

Output Low: 0.4 V max @ 3.2 mA sink; 0.5 V max @ 8.0 mA sink

Output High: 2.4 V min @ $-200\ \mu\text{A}$ source

Digital outputs: LSTTL compatible

Key Features

Software-selectable active-high or active-low counter inputs

Programmable hardware or software gating

One-shot or continuous output modes with retriggering

Programmed frequency output capability

8-bit latched digital input port

16-bit latched digital output port

Independent interrupt logic for each 5-channel half

External interrupt input with latching on rising edge

Interrupt level selection: software (3, 5, 7, 10, 11, 15) or jumper-selectable (2–7)

Typical Applications

Automated test system integration

Data acquisition setups

Precision frequency and pulse width measurement

Event counting and logging

Programmable waveform generation

Compatibility & Integration

Designed for ISA-bus compatible PCs including EISA and Pentium-based systems.

CTM Characteristics / Specifications

PARAMETER	VALUE
Model	CTM-10
Manufacturer	Keithley MetraByte
Instrument Type	ISA 10 channel counter timer board
Alias	CTM-10; CTM10
Host Bus	ISA
Counter Channels	10
Counter Width	16 bit
Timer Chips	two AMD 9513 class
Related Five Channel	CTM-05 (one 9513)
Max Count Rate Class	7 MHz family
Functions	event count, frequency, rate generation, one shot, repetitive one shot
Gate	programmable
Output Polarity	programmable
Duty Cycle	programmable (rate/one-shot)
Digital IO	9513 port bits family
Connector	counter/timer header to STA accessory class
Base Address	jumper/DIP
Interrupts	jumper selectable
Software	DriverLINX / MetraByte CTM
Form	ISA card
Clock Source	internal and external family
Compare	KPCI-3102 includes four user counter/timers on PCI Notes Identity from Keithley/MetraByte CTM-05 / CTM-10 counter-timer family (10 channels, dual 9513, 16-bit). Confirm CTM-10 versus CTM-05 channel count and 9513 max clock on the installed revision.

CTM Specifications

PARAMETER	VALUE
Programmable pulse and rate generation	ISA industrial timing
Key Features	Ten 16-bit counters (two 9513 devices) versus five on CTM-05
Technical Specifications	Model: CTM-10

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Specifications

PARAMETER	VALUE
Software	DriverLINX / MetraByte CTM
Form	ISA card
Clock Source	internal and external family

Compare: KPCI-3102 includes four user counter/timers on PCI

Notes

Identity from Keithley/MetraByte CTM-05 / CTM-10 counter-timer family (10 channels, dual 9513, 16-bit).
Confirm CTM-10 versus CTM-05 channel count and 9513 max clock on the installed revision.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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NIST-traceable calibration · SAM registered ·
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.