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Data Sheet

August 13, 2026

KEITHLEY

Keithley DAS-8PGA / DAS-8PG2 Analog-to-Digital Converter

The Keithley DAS-8PGA/DAS-8PG2 are analog-to-digital converters designed for data acquisition applications. These converters offer high-resolution conversion, programmable gain amplification, and versatile input/output capabilities. Ideal for scientific research, industrial automation, and quality control, the DAS-8PGA/DAS-8PG2 provide accurate and reliable data capture.



MODEL

Keithley DAS-8PGA/DAS-8PG2

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

DAS-8PGA/DAS-8PG2

LISTING

<https://aumictechlabs.com/products/das-8pga-das-8pg2-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Keithley DAS-8PGA and DAS-8PG2 are 8-channel, 12-bit successive approximation analog-to-digital converters designed for ISA bus-based data acquisition on IBM PC and compatible systems. Both models integrate programmable gain amplification, sample-and-hold circuitry, digital I/O, and counter/timer functionality in a single board, delivering flexible signal conditioning and acquisition capabilities for scientific research, industrial automation, and quality control applications. Technical Specifications Analog Input Subsystem 8 channels with 12-bit resolution and 25 μ s typical conversion time (35 μ s maximum) Switch-selectable single-ended or differential input configuration per channel Input impedance >1 G Ω typical; input current $\pm$ 30 nA @ 25°C,

± 250 nA over temperature Maximum overvoltage rating ± 35 V per channel DAS-8PGA: Software-programmable ranges of ± 10 V, ± 5 V, ± 0.5 V, ± 0.05 V, ± 0.01 V, 0 to +10 V, 0 to +1 V, 0 to +0.02 V DAS-8PG2: Software-programmable ranges of ± 5 V, ± 2.5 V, ± 1.25 V, ± 0.625 V, 0 to +5 V, 0 to +2.5 V, 0 to +1.25 V, 0 to +0.625 V Linearity ± 1 bit; throughput up to 4 ksamples/s with DriverLINX software Digital I/O 3-bit input port and 4-bit output port; TTL-compatible levels Non-clocked data transfer without hardware overrun detection Counter/Timer Three 16-bit internal counters using Intel 8254 or equivalent 82C54 chip Supports event counting, pulse and waveform generation, frequency, period, and pulse-width measurements Counter 2 clock source: internally selectable between PC bus clock or 1 MHz crystal oscillator Key Features Programmable gain amplification across eight input channels Integrated sample-and-hold per conversion cycle Flexible input range selection tailored to signal amplitude TTL-compatible digital I/O for system integration Multi-function counter/timer for timing and measurement tasks Typical Applications Data acquisition in laboratory environments, sensor signal digitization, industrial process monitoring, and test automation where programmable input conditioning and modest sampling rates are sufficient. Compatibility & Integration ISA bus architecture supports IBM PC and compatible systems. Supplied DriverLINX software provides software control and real-time sampling.

DAS8PGADAS8PG2 Characteristics / Specifications

PARAMETER	VALUE
Model	DAS-8PGA
Manufacturer	Keithley MetraByte
Instrument Type	ISA 8 channel programmable gain analog board
Alias	DAS-8PGA; DAS8PGA
Host Bus	ISA 8 bit
Analog Inputs	8
ADC Resolution	12 bit
Gain Select	programmable (PGA)
High Gain Set Class	1, 10, 100, 500 (PGA high-gain option)
Low Gain Set Class	1, 2, 4, 8 (PGA low-gain option)
Digital Inputs	3
Digital Outputs	4
Timer Counter	1 class
Connector	37 pin D-sub DAS-8 compatible
Related Fixed Gain	DAS-8 jumper gains
Related With AO	DAS-8/AO
Related 16 Channel	DAS-16G1 / DAS-16G2
Base Address	jumper/DIP
Interrupts	jumper selectable
Software	DAS-8 family mode calls; DriverLINX class
Form	8-bit ISA
Input Coupling	DC
STA Accessory	STA-08 class
Channel Expansion	not HC 64-channel; 8 inputs on board

DAS8PGADAS8PG2 Specifications

PARAMETER	VALUE
Applications	Mixed-level 8-channel ISA acquisition
Thermocouple/strain millivolt inputs at high gain	DAS-8 software migration with PGA
Key Features	Programmable gain per conversion versus DAS-8 jumpers
Digital I/O retained	8-bit ISA for XT-class PCs
Technical Specifications	Model: DAS-8PGA

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Instrument Type	ISA 8 channel programmable gain analog board
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Host Bus	ISA 8 bit
Analog Inputs	8
ADC Resolution	12 bit
Gain Select	programmable (PGA)
High Gain Set Class	1, 10, 100, 500 (PGA high-gain option)
Low Gain Set Class	1, 2, 4, 8 (PGA low-gain option)
Digital Inputs	3
Digital Outputs	4
Timer Counter	1 class
Connector	37 pin D-sub DAS-8 compatible
Related Fixed Gain	DAS-8 jumper gains
Related With A0	DAS-8/A0
Related 16 Channel	DAS-16G1 / DAS-16G2
Base Address	jumper/DIP
Interrupts	jumper selectable
Software	DAS-8 family mode calls; DriverLINX class
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Input Coupling	DC
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Channel Expansion	not HC 64-channel; 8 inputs on board

Notes

Specs from Keithley/MetraByte DAS-8PGA catalogs. Confirm whether the board is the high-gain or low-gain PGA variant from the silkscreen/gain table before applying millivolt sensors.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.