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Data Sheet

August 7, 2026

KEYSIGHT

Keysight (Agilent) 1630G Logic Analyzer With 65 Channels

Keysight (Agilent) 1630G Logic Analyzer With 65 Channels The HP/Agilent/Keysight 1630G is a classic interactive state/timing logic analyzer with 65 channels for 16-bit microprocessor development. State sampling to 25 MHz with 1024-word memory; timing sample periods from 10 ns (100 MHz) to 500 ms. Adds 1630G-only overview features: time-position profiling and module linkage histograms beyond the base 1630A/D histogram set. Microprocessor state/timing debug; software performance overview; interactive state-armed timing measurements. The HP/Agilent/Keysight 1630G is a classic interactive state/timing logic analyzer with 65 channels for 16-bit microprocessor development. State sampling to 25 MHz with 1024-word memory; timing sample periods from 10 ns (100 MHz) to 500 ms. Adds 1630G-only overview features: time-position profiling and module linkage histograms beyond the base 1630A/D histogram set. Microprocessor state/timing debug; software performance overview; interactive state-armed timing measurements.

No photo

MODEL

Keysight 1630G

CALIBRATION

**N/A — off-catalog
OEM datasheet**

CONDITION

**See OEM
documentation**

STATUS

**Not listed on
Aumictech
inventory**

RFQ / SKU

1630G

LISTING

**Off-catalog — OEM dat
asheet only**

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

Features & description

From manufacturer datasheet

Features

- Channel configurations (state / timing): 65/0; 0/8; 57/8
- State clock: to 25 MHz single phase/single edge; 20 MHz with ORed clocks/edges
- Timing clocks: 10 ns to 500 ms in 1-2-5 sequence ($\pm 0.01\%$ accuracy) – 100 MHz max sample rate class
- Memory: 1024 words acquisition depth
- Threshold: -9.9 V to $+9.9$ V in 0.1 V steps; accuracy 2.5% ± 120 mV
- Setup ≥ 20 ns; hold 0 ns; min swing 600 mV p-p; max input ± 40 V peak
- Glitch: min detectable ~ 5 ns; glitch mode halves timing channel count
- State resources: sequenced trigger with up to 4 resource terms; occurrence to 59999; store qualifiers
- Compare: edit compare to 16 words; full-trace compare mode (1630G)
- Overview (1630G): time-position measurements; linkage histograms; state/time interval histograms
- Probes supplied class: three 10271A 9-bit state, one 10272A 8-bit state/timing, three 10273A 10-bit state
- Power: 115/230 Vac, 48–66 Hz, 275 W max; weight ~ 13.6 kg net / ~ 18.1 kg shipping
- Operating temperature: 0 to 55 °C

1630G Characteristics / Specifications

PARAMETER	VALUE
Model	1630G
Manufacturer	Keysight Technologies (Agilent / HP)
Instrument Type	Logic Analyzer
Family	1630 Series
Alias	1630G; Agilent 1630G; HP 1630G Digital Capture 65 ch max; 25 MHz state; timing to 100 MHz (10 ns); 1K memory Operational Notes Prefer HP 1630A/D/G Operating and Programming Manual 01630-90915. Channel assignment depends on selected state/timing configuration.
Channel configurations (state / timing)	65/0; 0/8; 57/8
State clock	to 25 MHz single phase/single edge; 20 MHz with ORed clocks/edges
Timing clocks	10 ns to 500 ms in 1-2-5 sequence ($\pm 0.01\%$ accuracy) – 100 MHz max sample rate class
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Operating temperature	0 to 55 °C

1630G Specifications

PARAMETER	VALUE
Keysight (Agilent) 1630G Logic Analyzer With 65 Channels	Overview
Applications	Microprocessor state/timing debug; software performance overview; interactive state-armed timing measurements.
Key Features	- Channel configurations (state / timing): 65/0; 0/8; 57/8
Technical Specifications	Model: 1630G
Digital Capture	65 ch max; 25 MHz state; timing to 100 MHz (10 ns); 1K memory
Operational Notes	Prefer HP 1630A/D/G Operating and Programming Manual 01630-90915. Channel assignment depends on selected state/timing configuration.

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Operating temperature	0 to 55 °C
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Model	1630G
Manufacturer	Keysight Technologies (Agilent / HP)
Instrument Type	Logic Analyzer
Family	1630 Series
Alias	1630G; Agilent 1630G; HP 1630G Digital Capture 65 ch max; 25 MHz state; timing to 100 MHz (10 ns); 1K memory Operational Notes Prefer HP 1630A/D/G Operating and Programming Manual 01630-90915. Channel assignment depends on selected state/timing configuration.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

performance overview; interactive state-armed timing measurements.

Key Features

- Channel configurations (state / timing): 65/0; 0/8; 57/8
- State clock: to 25 MHz single phase/single edge; 20 MHz with 0Red clocks/edges
- Timing clocks: 10 ns to 500 ms in 1-2-5 sequence (±0.01% accuracy) – 100 MHz max sample rate class
- Memory: 1024 words acquisition depth
- Threshold: -9.9 V to +9.9 V in 0.1 V steps; accuracy 2.5% ±120 mV
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Technical Specifications

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PARAMETER	VALUE
Model	1630G
Manufacturer	Keysight Technologies (Agilent / HP)
Instrument Type	Logic Analyzer
Family	1630 Series
Alias	1630G; Agilent 1630G; HP 1630G

Digital Capture

65 ch max; 25 MHz state; timing to 100 MHz (10 ns); 1K memory

Operational Notes

Prefer HP 1630A/D/G Operating and Programming Manual 01630-90915. Channel assignment depends on selected state/timing configuration.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: N/A — off-catalog OEM datasheet. See OEM documentation · Not listed on Aumictech inventory.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.