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Data Sheet

August 13, 2026

KEYSIGHT / AGILENT

Keysight (Agilent) 1671A Timing Benchtop Deep Memory Logic Analyzer, 102-Channel, 70 MHz State/250 MHz

Keysight (Agilent / HP) 1671A Timing Benchtop Deep Memory Logic Analyzer The HP/Agilent/Keysight 1671A is a 102-channel benchtop deep-memory logic analyzer in the 1670A Series. It provides 70 MHz state analysis and 250 MHz timing analysis with 64K samples of acquisition memory standard (500K optional, option 030), Ethernet LAN, and large internal mass storage so teams can correlate long microprocessor traces with source onanetworked PC or workstation. Embedded microprocessor hardware/software integration; long-duration state and timing debug; inverse-assembly mnemonic viewing; LAN-based source correlation and remote file transfer. The HP/Agilent/Keysight 1671A is a 102-channel benchtop deep-memory logic analyzer in the 1670A Series. It provides 70 MHz state analysis and 250 MHz timing analysis with 64K samples of acquisition memory standard (500K optional, option 030), Ethernet LAN, and large internal mass storage so teams can correlate long microprocessor traces with source onanetworked PC or workstation. Embedded microprocessor hardware/software integration; long-duration state and timing debug; inverse-assembly mnemonic viewing; LAN-based source correlation and remote file transfer.



MODEL

**Keysight / Agilent
1671A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

1671A

LISTING

[https://aumictechlabs.c
om/products/1671a-2/](https://aumictechlabs.com/products/1671a-2/)

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Keysight (Agilent) 1671A is a 102-channel benchtop logic analyzer designed for debugging and validation of complex digital systems, particularly embedded microprocessor architectures. It captures extended operational sequences through deep memory acquisition, correlate high-level language code with real-time processor traces and analyze critical hardware timing relationships over prolonged periods. The instrument combines state and timing analysis capabilities with integrated mass storage and network connectivity for laboratory and remote operation.

Technical Specifications

Channels: 102 State Speed: 70 MHz Timing Speed: 250 MHz Acquisition Memory: 64K standard per channel; 500K optional per channel Mass Storage: Internal hard drive Removable Media: 3.5-inch high-density floppy disk (DOS and LIF formats) Network Interface: 10Base-T and 10Base-2 LAN connectors Programmable Interfaces: HP-IB and RS-232 Printer Port: Centronics Form Factor: Benchtop Operating Environment: Indoor use only Humidity Limit: Non-condensing

Key Features

Deep memory acquisition enables extended capture and processor mnemonic analysis over sustained operational windows Simultaneous time-correlated state and timing analysis across configurable channel subsets Source code correlation with C++ and high-level languages via standard Ethernet interface for real-time microprocessor trace capture Transitional timing mode conserves acquisition memory by storing samples only on data state changes Remote operation support via X-Windows and data transfer protocols (FTP/NFS) Data export in ASCII format; screen captures in PCX, TIFF, and EPS formats

Typical Applications

Embedded microprocessor system debugging and hardware/software defect identification Real-time processor trace correlation with host application code execution Complex digital system validation and timing verification Extended-duration behavioral analysis of digital circuits

Compatibility & Integration

The 1671A integrates into laboratory environments through LAN-based remote operation, Centronics printer connectivity, and standard laboratory programming interfaces (HP-IB, RS-232). When deployed in a 16700-series mainframe, power is supplied through the mainframe backplane connector.

Features & description

From manufacturer datasheet

Features

- 102 channels onastandalone benchtop frame

1671A Characteristics / Specifications

PARAMETER	VALUE
Model	1671A
Manufacturer	Keysight Technologies (Agilent / HP)
Instrument Type	Benchtop deep-memory logic analyzer
Family	1670A Series
Alias	1671A; Agilent 1671A; HP 1671A
Channels	102
State analysis speed	70 MHz
Timing analysis speed	250 MHz
Memory depth (standard)	64K samples per channel class
Memory depth (option 030)	500K samples
Form factor	Benchtop (rackmount kit option 1CM)
LAN	Ethernet 10Base-T (RJ-45) and 10Base-2 (BNC)
LAN protocols	TCP/IP; FTP; NFS; X11
Other I/O	Centronics; RS-232; HP-IB
Floppy drive	3.5-inch high-density DOS/LIF
Mass storage	Internal hard drive
Documentation export	ASCII data; TIFF/PCX/PostScript screen images
Trigger UI	Graphical trigger macros
Related models	1670A 136-ch; 1672A 68-ch; 1673A 34-ch
Primary use case	Embedded MPU state/timing with source correlation Notes Specs from HP/Agilent 1671A product descriptions and 1670A-series LAN user literature. Confirm option 030 memory depth and installed inverse assemblers on the unit. Do not confuse with later 1671E/G performance classes.
Related family channel counts	1670A (136), 1672A (68), 1673A (34)

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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Notes

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Ordering Information

OPTION	DESCRIPTION
Option 030	memory depth and installed inverse assemblers on the unit. Do not confuse with later 1671E/G performance classes.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.