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Data Sheet

August 13, 2026

KEYSIGHT / AGILENT / HP

Keysight / Agilent / HP E1430A VXI 10 MSa/s 23-bit Digitizer

Agilent E1430A VXI 10 MSa/s 23-bit Digitizer with Filter and Memory The Agilent E1430A isaC-size VXI analog-to-digital converter module with a 23-bit-resolution 10 MSa/s (10.24 MHz class) ADC, octave-spaced anti-alias filters, a complex frequency shifter, and 8 Mbyte FIFO memory. It captures band-limited analog signals for DSP and computer analysis with register-based or ECL local-bus data paths. High-dynamic-range VXI digitizing; IF/baseband capture with anti-alias filtering; multi-module local-bus streaming to a consumer module; precision ADC in ATE. The Agilent E1430A isaC-size VXI analog-to-digital converter module with a 23-bit-resolution 10 MSa/s (10.24 MHz class) ADC, octave-spaced anti-alias filters, a complex frequency shifter, and 8 Mbyte FIFO memory. It captures band-limited analog signals for DSP and computer analysis with register-based or ECL local-bus data paths. High-dynamic-range VXI digitizing; IF/baseband capture with anti-alias filtering; multi-module local-bus streaming to a consumer module; precision ADC in ATE.



MODEL

**Keysight / Agilent /
HP E1430A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

E1430A

LISTING

<https://aumictechlabs.com/products/e1430a-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Keysight E1430A is a C-size VXI digitizer module delivering 10 MSa/s sampling with 23-bit resolution for precision measurement and signal analysis. This register-based, one-slot

instrument combines a two-pass digitizer architecture with DSP-based real-time digital filtering, programmable gain conditioning, and large FIFO memory to capture transients, analyze spectra, digitize intermediate frequencies, and record complex waveforms across demanding applications.

Technical Specifications

Sampling Rate: 10 MSa/s maximum

Resolution: 23-bit raw

Dynamic Range: 18 bits (110 dBFS) distortion-free, spur-free dynamic range with dithering and on-the-fly distortion correction

Noise Density: -136 dBfs/Hz

Noise Figure: 15 dB

Input Impedance: 50 ohms

Input Range: +28 dBm to -32 dBm, programmable in 6 dB steps

Anti-Alias Filters: Built-in; 4 MHz maximum information bandwidth; selectable bypass for fastest rise times

Digital Filters: Multistage, programmable with decimation; bandwidths from 4 MHz to 0.24 Hz in octave steps

Digital Local Oscillator: Tunable for signal-centric filtering and unwanted signal rejection

Memory: 8 MB high-speed FIFO for transient capture and block-mode data transfer

Signal Conditioning: Flexible input signal conditioning included

Local Bus Data Transfer: 40 Mbytes/sec

Key Features

Two-pass sub-ranging digitizer architecture

Programmable gain in 6 dB steps across wide dynamic range

DSP-based real-time filtering with independently tunable digital LO

Octave-step digital filter bandwidth control for noise optimization

Large FIFO memory supporting continuous acquisition and transient recording

Standard VXI data path and high-speed Local Bus connectivity

Typical Applications

Spectrum analysis and IF digitization

Transient capture and waveform recording

High-resolution time-domain measurements

Low-noise signal conditioning and filtering

Compatibility & Integration

VXI-based architecture with VXIplug&play compatibility. This product is obsolete; manuals are provided for reference only.

Features & description

From manufacturer datasheet

Features

- 23-bit outputs at up to 10.24 MHz sample rate

E1430A Characteristics / Specifications

PARAMETER	VALUE
Model	E1430A
Manufacturer	Agilent / Hewlett Packard / Keysight
Instrument Type	VXI Digitizer ADC
Alias	E1430A; HP E1430A
Maximum Sample Rate	10 MSa/s (10.24 MHz class)
ADC Resolution	23 bit
Memory	8 Mbyte FIFO
Filters	octave-spaced anti-alias family
Frequency Shifter	complex (digital)
Noise Floor Class	-70 dB rms vs full-scale sine (random uncorrelated)
Quantization	amplitude resolution beyond analog noise (quantization negligible)
Local Bus Rate	up to 80 Mbyte/s ECL local bus
Local Bus Width	8 bit ECL
Local Bus Direction	output to adjacent module on the right
VME Path	register based unpacked to 16-bit words
VXI Form	C-size
Front Analog	BNC analog input
Clock	front-panel clock BNC class
Trigger	front-panel trigger BNC class
Data Consumer	requires local-bus consumer for 80 Mbyte/s path
Protocol	Agilent ECL local bus
Use Case	band-limited continuous analog capture
Related E1432A	16 channel 51.2 kSa/s + DSP
Related E1438D	100 MSa/s DSP digitizer
Related E1428A	1 GSa/s 8-bit scope

PARAMETER	VALUE
Status	HP/Agilent VXI E1430A EOL
Host	C-size VXI mainframe Slot 0 command module required in the cage
Cooling	VXI mainframe airflow per C-size module Notes Architecture from HP/Agilent E1430A 10 MSa/s ADC product description. Confirm local-bus consumer module if using 80 Mbyte/s streaming. Finer analog input-range tables belong on the E1430A data sheet for the serial prefix.

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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<https://aumictechlabs.com>

NIST-traceable calibration · SAM registered ·
30-day returns
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.