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Data Sheet

August 13, 2026

KEYSIGHT / AGILENT / HP

Keysight / Agilent / HP E2925A 32-Bit 33 MHz PCI Exerciser and Analyzer

Agilent E2925A 32-bit 33 MHz PCI Exerciser and Analyzer The Agilent E2925A is a 32-bit 33 MHz PCI exerciser and analyzer card for PCI protocol generation and capture. Master/target traffic generation with simultaneous analysis. Predecessor/sibling to E2928A 66 MHz and E2927A 32/64-bit 66 MHz cards. Used for PCI silicon and board verification. PCI 33 MHz 32-bit compliance and bring-up; master/target exerciser patterns; protocol capture and trigger; automated C-API test. The Agilent E2925A is a 32-bit 33 MHz PCI exerciser and analyzer card for PCI protocol generation and capture. Master/target traffic generation with simultaneous analysis. Predecessor/sibling to E2928A 66 MHz and E2927A 32/64-bit 66 MHz cards. Used for PCI silicon and board verification. PCI 33 MHz 32-bit compliance and bring-up; master/target exerciser patterns; protocol capture and trigger; automated C-API test.



MODEL

**Keysight / Agilent /
HP E2925A**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

E2925A

LISTING

[https://aumictechlabs.c
om/products/e2925a-2/](https://aumictechlabs.com/products/e2925a-2/)

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Keysight/Agilent/HP E2925A is a 32-bit, 33 MHz PCI exerciser and analyzer that functions as both master and target device for comprehensive PCI bus validation. It delivers peak throughput of 107 MB/s (read) and 102 MB/s (write) in zero-wait-state burst mode, perform complex traffic emulation, protocol debugging, and stress testing of PCI-based designs. The instrument

combines a PCI state logic analyzer with real-time protocol checking against 25 PCI Specification Rev 2.1 rules, performance measurement capabilities, and sophisticated traffic generation for complex scenarios and worst-case patterns. Technical Specifications PCI Bus: 32-bit, 33 MHz Peak Throughput (Read): 107 MB/s Peak Throughput (Write): 102 MB/s Burst Mode: Zero-Wait-State Trace Memory: 32 kB standard (1 MB optional) On-board Emulation Memory: 128 kB for master and target resources Protocol Rules Monitored: 25 (PCI Specification Rev 2.1) Interrupt Generation: INTA# through INTD# Form Factor: PCI Card Key Features PCI-oriented triggering and storage qualification with optional 64-branch trigger sequencer Real-time performance measurement including efficiency, throughput, utilization, and user-defined metrics Full protocol control for master and target emulation with traffic analysis On-board CPU with built-in system data integrity test functions Programmable configuration space and expansion EEPROM CPU port and static I/O signal access for DUT register and signal monitoring beyond PCI Ready-to-run stress tests and protocol permutation library Control and Interface C-Application Programming Interface (C-API) for test environment integration Control via RS232, PCI, or bi-directional Centronics Windows 95/NT-based graphical user interface (add-on) Typical Applications PCI chip, card, and system development and verification; protocol compliance validation; design reliability assessment under stress conditions; traffic pattern emulation for debugging complex PCI interactions.

Features & description

From manufacturer datasheet

Features

- 32-bit 33 MHz PCI (not 66 MHz 64-bit)

E2925A Characteristics / Specifications

PARAMETER	VALUE
Model	E2925A
Manufacturer	Agilent / Hewlett Packard / Keysight
Instrument Type	PCI Exerciser and Analyzer
Family	E2920
Alias	E2925A; Agilent E2925A
PCI Width	32 bit
PCI Clock	33 MHz
Roles	master and target
Functions	exerciser and analyzer
Related E2928A	66 MHz PCI exerciser/analyzer
Related E2927A	32/64-bit 66 MHz (512 kB data memory 64-level sequencer)
Host	PCI slot in DUT or extender
Software	Agilent PCI exerciser GUI and C-API
Protocol Check	real-time PCI protocol checking family
Trigger	sequencer/pattern terms family
Trace	on-board state trace memory
Form	PCI add-in test card
Power	PCI backplane
Status	Agilent E2925A EOL
NotAGPIB Card	protocol exerciser not 82350
Not PCIe	conventional PCI
Not 66 MHz	see E2928A for 66 MHz
Compliance	PCI 2.x 33 MHz 32-bit class
Automation	C-API
Stress	autonomous patterns family

PARAMETER	VALUE
GUI	included family software
Documentation	E2925A user/programming Notes 32-bit 33 MHz PCI exerciser/analyzer identity from Agilent E2925A product literature. Memory depth and sequencer levels vary by E292x model: do not copy E2927A 4 MB optional trace onto E2925A without the E2925A sheet.

E2925A Specifications

PARAMETER	VALUE
Applications	PCI 33 MHz 32-bit compliance and bring-up; master/target exerciser patterns; protocol capture and trigger; automated C-API test.
Key Features	- 32-bit 33 MHz PCI (not 66 MHz 64-bit)
Technical Specifications	Model: E2925A

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
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PARAMETER	VALUE
Model	E2925A
Manufacturer	Agilent / Hewlett Packard / Keysight
Instrument Type	PCI Exerciser and Analyzer
Specifications	
PARAMETER	VALUE
Family	E2920
Alias	E2925A; Agilent E2925A
PCI Width	32 bit
Specifications	
PARAMETER	VALUE
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Specifications

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Specifications

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Specifications

PARAMETER	VALUE
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Documentation	E2925A user/programming

Notes

32-bit 33 MHz PCI exerciser/analyzer identity from Agilent E2925A product literature. Memory depth and sequencer levels vary by E292x model: do not copy E2927A 4 MB optional trace onto E2925A without the E2925A sheet.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.