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Data Sheet

August 12, 2026

LECROY

LeCroy M5X Protocol Analyzer for PCI Express® 5.0

Teledyne LeCroy Summit M5x Protocol Analyzer / Jammer (PCIe 5.0) The Summit M5x is Teledyne LeCroy's PCIe protocol analyzer/jammer platform for PCIe 4.0/5.0, Gen-Z, and related high-speed I/O. Supports data rates to 32 GT/s with large trace memory and CATC Trace analysis software. (Product line discontinued; successor Summit M6xx family.) PCIe/NVMe bring-up; RAS error injection; Gen-Z/CCIX analysis; server and add-in card protocol debug. The Summit M5x is Teledyne LeCroy's PCIe protocol analyzer/jammer platform for PCIe 4.0/5.0, Gen-Z, and related high-speed I/O. Supports data rates to 32 GT/s with large trace memory and CATC Trace analysis software. (Product line discontinued; successor Summit M6xx family.) PCIe/NVMe bring-up; RAS error injection; Gen-Z/CCIX analysis; server and add-in card protocol debug.



MODEL

Lecroy M5X

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

M5X

LISTING

<https://aumictechlabs.com/products/m5x-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Teledyne LeCroy M5X Protocol Analyzer for PCI Express® 5.0 captures, decodes, and analyzes PCIe traffic across multiple generations and link configurations. Built for engineers developing high-speed serial interfaces, it delivers protocol compliance verification and design debugging across data rates from 2.5 GT/s to 32.0 GT/s. The analyzer supports PCIe 4.0 at x16

widths and PCIe 5.0 at x8 widths, with configurable memory up to 128 GB for extended recordings and full layer decoding from FLIT through CXL Message levels. Technical Specifications Data Rates: 2.5 GT/s, 5.0 GT/s, 8.0 GT/s, 16.0 GT/s, 32.0 GT/s Link Width Support: PCIe 4.0 up to x16; PCIe 5.0 up to x8. Direct tapping at 32.0 GT/s supports up to x8. Inline probing supports x1, x2, x4, x8, x16 up to 16.0 GT/s Memory Buffer: Configurable up to 128 GB Control Interface: USB or 1000-BaseT Ethernet (100/1000 Mbps) Operating Systems: Windows 10, Windows 8, Windows 7, Windows Server, Windows Server 2008R2 Key Features Protocol Support: PCIe 4.0/5.0, Gen-Z, CCIX, CXL (CXL.io, CXL.mem, CXL.cache), NVMe, NVMe-MI, SATA Express, SCSI Express Jamming Capability: Protocol-compliant packet modification, insertion, replacement, and deletion within PCIe operating specifications; supports Gen-Z jamming Multi-Protocol Decoding: Transaction Layer Packets (TLPs), Data Link Layer Packets (DLLPs), primitives, and full CXL layer decoding from FLIT to Message layers Probing Options: Inline and direct tapping methodologies; horizontal insertion design eliminates traditional interposer requirements for server systems CrossSync Synchronization: Time-aligned packet capture and cross-triggering with other Teledyne LeCroy high-speed protocol analyzers Typical Applications computing, cloud infrastructure, AI system development, and PCIe 5.0 endpoint/root complex validation. Compatibility & Integration Connects via USB or Ethernet. Integrates with Teledyne LeCroy analyzer portfolio through CrossSync for multi-bus correlation.

Features & description

From manufacturer datasheet

Features

- Data rates: 2.5, 5.0, 8.0, 16.0 GT/s; also 32.0 GT/s in PCIe 5.0 mode
- Link width: up to x16 for PCIe 4.0 / Gen-Z / CCIX; up to x8 for PCIe 5.0
- Trace memory: configurable up to 128 GB
- Modes: analyzer; analyzer/jammer (inline); PCIe 4.0 RAS error injection; NVMe / NVMe-MI jamming; Gen-Z jamming
- Control: USB; 1000BASE-T Ethernet remote LAN
- Sync: CrossSync with other Teledyne LeCroy protocol analyzers
- Views: CATC Trace; BitTracer raw PHY-layer recording
- Ports (literature): DS/US ref clocks, USB 3.0 Type-B, trigger I/O, 1 GbE, sync I/O
- Front panel: graphic status displays + SPEED indicators for 2.5/5/8/16 GT/s class
- Probing: CEM interposers; multi-lead; non-interposer horizontal-insertion methods per OEM

M5X Characteristics / Specifications

PARAMETER	VALUE
Model	M5x
Manufacturer	Teledyne LeCroy
Instrument Type	PCI Express Protocol Analyzer / Jammer
Alias	Summit M5x; LeCroy M5x; PE M5x Performance
Rates	2.5–32 GT/s
Width	to x16 (Gen4); to x8 (Gen5)
Memory	up to 128 GB Notes Specs from Teledyne LeCroy Summit M5x product pages and datasheet literature. Exact licenses, memory size, and probe set are configuration-dependent—confirm on the unit.
Data rates	2.5, 5.0, 8.0, 16.0 GT/s; also 32.0 GT/s in PCIe 5.0 mode
Link width	up to x16 for PCIe 4.0 / Gen-Z / CCIX; up to x8 for PCIe 5.0
Trace memory	configurable up to 128 GB
Modes	analyzer; analyzer/jammer (inline); PCIe 4.0 RAS error injection; NVMe / NVMe-MI jamming; Gen-Z jamming
Control	USB; 1000BASE-T Ethernet remote LAN
Sync	CrossSync with other Teledyne LeCroy protocol analyzers
Views	CATC Trace; BitTracer raw PHY-layer recording
Ports (literature)	DS/US ref clocks, USB 3.0 Type-B, trigger I/O, 1 GbE, sync I/O
Front panel	graphic status displays + SPEED indicators for 2.5/5/8/16 GT/s class
Probing	CEM interposers; multi-lead; non-interposer horizontal-insertion methods per OEM

M5X Specifications

PARAMETER	VALUE
Key Features	- Data rates: 2.5, 5.0, 8.0, 16.0 GT/s; also 32.0 GT/s in PCIe 5.0 mode
Technical Specifications	Model: M5x
Performance	Rates: 2.5–32 GT/s

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

Specifications

PARAMETER	VALUE
Model	M5x
Manufacturer	Teledyne LeCroy
Instrument Type	PCI Express Protocol Analyzer / Jammer
Alias	Summit M5x; LeCroy M5x; PE M5x

Performance

Performance

PARAMETER	VALUE
Rates	2.5–32 GT/s
Width	to x16 (Gen4); to x8 (Gen5)
Memory	up to 128 GB

Notes

Specs from Teledyne LeCroy Summit M5x product pages and datasheet literature. Exact licenses, memory size, and probe set are configuration-dependent-confirm on the unit.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.