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Data Sheet
August 12, 2026

MARCONI / IFR / AEROFLEX

Marconi IFR Aeroflex FB100A Bit Error Rate Tester

Aeroflex / IFR / Marconi FB100A Fastbit BER Test System The Aeroflex IFR (Marconi heritage) FB100A isamodular high-rate bit-error-rate test system (“Fastbit”) for R&D and manufacturing of modems, decoders, and digital links. Operates DC to 160 Mb/s with serial and/or parallel interfaces and INSTALOK synchronization. Modem/demodulator and set-top evaluation boards; flexible BER for packet/frame testing; optional broadband impairment (C/N, noise, fading). The Aeroflex IFR (Marconi heritage) FB100A isamodular high-rate bit-error-rate test system (“Fastbit”) for R&D and manufacturing of modems, decoders, and digital links. Operates DC to 160 Mb/s with serial and/or parallel interfaces and INSTALOK synchronization. Modem/demodulator and set-top evaluation boards; flexible BER for packet/frame testing; optional broadband impairment (C/N, noise, fading).

MODEL **Marconi / IFR / Aeroflex FB100A**	CALIBRATION **Available on request**
CONDITION **Used**	STATUS **In stock**
RFQ / SKU **FB100A**	LISTING **https://aumictechlabs.com/products/fb100a-2/**

NIST-traceable cal · SAM registered · 30-day returns

NAICS 423690 · 811210 · 541380

This unit

The Marconi IFR Aeroflex FB100A is a modular bit error rate tester engineered for performance assessment of digital communication systems across telecommunications and data communication applications. It generates and analyzes pseudo-random binary sequences (PRBS) to measure transmission link bit error ratio (BER), supporting both serial and parallel testing topologies. The instrument accommodates system-level, sub-system, and component testing during development and compliance verification phases.Technical Specifications Maximum data

rate: 100 Mbit/s (serial), 160 Mbit/s (parallel) Maximum clock rate: 100 MHz Pattern depth: 4 Mbit Independent data generator and analyzer: 4 Mbyte memory each RF channel impairment generation: 5 MHz to 2.4 GHz tunable noise Carrier power levels: down to -70 dBm (standard); -140 dBm with OP240/OP240B option C/N and C/I ratio resolution: 0.1 dB External interferer capability: up to four signals, 5 MHz to 2.4 GHzKey Features INSTALOK™ synchronization technology for rapid data synchronization and error code analysis Resident interfaces: TTL, ECL, PECL Interface pods: TTL, LVDS, RS-422 (V.11), MPEG SPI, ASI, OC3, HSSI, X.21, RS-449, V.35, V.36, RS-232, EIA/TIA-530/A Logic level outputs: TTL and PECL (serial); byte-width data and pilot wire (parallel) Signal connection options: Pod-to-Header and Pod-to-Micro-grabbers for circuit card and device probing Burst noise and flat fading carrier profiles Selectable channel bandwidths Automatic BER versus Eb/No measurement Windows NT user interfaceTypical Applications Digital communication system performance validation Telecommunications component and subsystem testing Compliance verification and certification RF front-end simulation with broadband noise loading Co-channel, adjacent channel, and total interferer power testingCompatibility & Integration The FB100A supports diverse transmission standards and interface protocols through resident interfaces and optional interface pods, enabling integration into complex test environments requiring multi-standard BER measurement and RF impairment simulation.

Features & description

From manufacturer datasheet

Features

• Data rate: DC to 160 Mb/s (serial and/or parallel class) • Pattern / memory: programmable pattern depth to 4 Mbit class; independent ~4 MB generator and analyzer memories (literature) • Sync: INSTALOK technology • Resident logic: TTL, ECL, PECL • Interface pods (options): TTL, LVDS, RS-422 (V.11), MPEG SPI, ASI, OC3, HSSI; telecom pod X.21 / RS-449 / V.35 / V.36 / RS-232 / EIA/TIA-530/A • Parallel width: serial + 8-bit parallel on TTL pod; 8-bit on LVDS / RS-422 pods • Connection styles: pod-to-header and pod-to-micrograbbers for board probing • Impairment options: tunable noise 5 MHz–2.4 GHz; C/N and interference; burst noise; flat fading; automatic BER vs C/N • UI: Windows NT-era user interface (OEM literature)

FB100A Characteristics / Specifications

PARAMETER	VALUE
Model	FB100A
Manufacturer	Aeroflex / IFR / Marconi
Instrument Type	Bit Error Rate Test System
Alias	FB100A; Fastbit FB100A; Aeroflex FB100A; IFR FB100A Performance
Rate	DC–160 Mb/s
Pattern depth	4 Mbit class Interfaces
Resident	TTL / ECL / PECL
Pods	per option fit Notes Specs from Aeroflex/IFR FB100A product literature. Exact rate limits and pod set depend on configuration; confirm installed pods/options on the unit. Some reseller tables list lower serial rates–prefer 160 Mb/s system claim when present.
Data rate	DC to 160 Mb/s (serial and/or parallel class)
Pattern / memory	programmable pattern depth to 4 Mbit class; independent ~4 MB generator and analyzer memories (literature)
Sync	INSTALOK technology
Resident logic	TTL, ECL, PECL
Interface pods (options)	TTL, LVDS, RS-422 (V.11), MPEG SPI, ASI, OC3, HSSI; telecom pod X.21 / RS-449 / V.35 / V.36 / RS-232 / EIA/TIA-530/A
Parallel width	serial + 8-bit parallel on TTL pod; 8-bit on LVDS / RS-422 pods
Connection styles	pod-to-header and pod-to-micrograbbers for board probing
Impairment options	tunable noise 5 MHz–2.4 GHz; C/N and interference; burst noise; flat fading; automatic BER vs C/N

FB100A Specifications

PARAMETER	VALUE
Aeroflex / IFR / Marconi FB100A Fastbit BER Test System	Overview
Applications	Modem/demodulator and set-top evaluation boards; flexible BER for packet/frame testing; optional broadband impairment (C/N, noise, fading).
Key Features	- Data rate: DC to 160 Mb/s (serial and/or parallel class)
Technical Specifications	Model: FB100A
Performance	Rate: DC–160 Mb/s

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

Specifications

PARAMETER	VALUE
Model	FB100A
Manufacturer	Aeroflex / IFR / Marconi
Instrument Type	Bit Error Rate Test System
Alias	FB100A; Fastbit FB100A; Aeroflex FB100A; IFR FB100A

Performance
Rate: DC–160 Mb/s
Pattern depth: 4 Mbit class

Interfaces
Resident: TTL / ECL / PECL
Pods: per option fit

Notes
Specs from Aeroflex/IFR FB100A product literature. Exact rate limits and pod set depend on configuration; confirm installed pods/options on the unit. Some reseller tables list lower serial rates–prefer 160 Mb/s system claim when present.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: **sales@aumictech.com** · +1 (484) 841-9341

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