

**Aumictech**

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Data Sheet

August 12, 2026

TTC

TTC T-BERD 310-1 DS1/DSO Analyzer

The TTC T-BERD 310-1 is a DS1/DSO analyzer designed for testing and troubleshooting digital communication circuits. It provides comprehensive testing capabilities for DS1 and DSO level signals, including signal generation, measurement, and analysis. Ideal for telecom engineers and technicians.



MODEL

TTC T-BERD 310-1

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

T-BERD 310-1

LISTING

<https://aumictechlabs.com/products/t-berd-310-1-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The TTC T-BERD 310-1 DS1/DSO Analyzer is a modular digital communications test instrument engineered for DS1 and DSO signal generation, measurement, and analysis across telecommunications infrastructure. It addresses testing requirements in central offices, laboratories, mobile telephone switching offices, and manufacturing environments with field-upgradeable architecture supporting evolving network demands. Technical Specifications The 310-1 configuration delivers DS1/DSO analysis with DS1 scanning and automatic payload detection. It measures transmit and receive frequency, signal level, and power across digital circuits. Pulse shape analysis conforms to 1991 and 1993 ANSI and ITU-T specifications for DS3 circuits, enabling correlation of error bursts with level dropouts and frequency variations during installation. Jitter measurements extend to DS3 and SONET signals, including in-service DS3 jitter detection for SONET SPE pointer adjustment analysis. Frequency spectrum analysis operates on

demodulated jitter output.

Key Features

- DS1 scan with automatic payload detection and ESF data link analysis
- Programmable loop codes and test patterns; DS1 timing slips measurement
- Fractional T1 testing for leased contiguous and non-contiguous channels
- Automatic detection of received AMI or B8ZS coded signals; transmit capability in both formats
- DS1 timing reference from SONET, DS3, DS1, or BITS access points
- DS0 voice quality verification and signaling bit analysis via drop from DS1 to OC-12 access points
- BITS timing analysis for asynchronous and synchronous network impairment isolation
- ESF synchronization message decoding for BITS clock source assessment
- Complete pulse shape analysis for DS3 conformance verification
- ATM traffic simulation with advanced error insertion; ATM analysis options available
- SONET/SDH support for OC-3, OC-12, OC-48, and OC-48c signals with automatic conformance testing
- DWDM and optical media testing capabilities

Typical Applications

- DS1/DSO circuit troubleshooting, BITS timing isolation, fractional T1 leased channel validation, ATM traffic simulation, and SONET/SDH conformance verification.

Compatibility & Integration

Modular T-BERD 310 platform architecture enables field upgrades and configuration customization. Direct access to SONET, DS3, DS1, and BITS timing references.

Features & description

From manufacturer datasheet

Key Features

DS1 scan with automatic payload detection and ESF data link analysis Programmable loop codes and test patterns; DS1 timing slips measurement Fractional T1 testing for leased contiguous and non-contiguous channels Automatic detection of received AMI or B8ZS coded signals; transmit capability in both formats DS1 timing reference from SONET, DS3, DS1, or BITS access points DS0 voice quality verification and signaling bit analysis via drop from DS1 to OC-12 access points BITS timing analysis for asynchronous and synchronous network impairment isolation ESF synchronization message decoding for BITS clock source assessment Complete pulse shape analysis for DS3 conformance verification ATM traffic simulation with advanced error insertion; ATM analysis options available SONET/SDH support for OC-3, OC-12, OC-48, and OC-48c signals with automatic conformance testing DWDM and optical media testing capabilities Typical Applications DS1/DSO circuit troubleshooting, BITS timing isolation, fractional T1 leased channel validation, ATM traffic simulation, and SONET/SDH conformance verification. Compatibility & Integration Modular T-BERD 310 platform architecture enables field upgrades and configuration customization. Direct access to SONET, DS3, DS1, and BITS timing references.

TTC T-BERD 310-1 — Repair & Calibration Service

Aumictech offers NIST-traceable calibration and repair for the TTC T-BERD 310-1. All calibrations are performed to NIST-traceable standards and ship with a certificate. Turnaround is typically 1–3 business days after receipt. Standard Cal Pass/fail cert + NIST sticker Cal with Data Full NIST cert with measured values [Request Calibration](#) → [Request Repair Quote](#) →

TTC T-BERD 310-1 price

This TTC T-BERD 310-1 is listed at . Calibration is available separately from . Contact us for volume pricing or if you need multiple units.

TTC T-BERD 310-1 calibration cost

NIST-traceable calibration for the TTC T-BERD 310-1 is (standard — pass/fail certificate and calibration sticker) or (full measured data at every test point, reference standards documented, traceability chain included). Turnaround is 1 to 3 business days after receipt. See full calibration details →

Unit notes

The TTC T-BERD 310-1 DS1/DSO Analyzer is a modular digital communications test instrument engineered for DS1 and DSO signal generation, measurement, and analysis across telecommunications infrastructure. It addresses testing requirements in central offices, laboratories, mobile telephone switching offices, and manufacturing environments with field-upgradeable architecture supporting evolving network demands. Technical Specifications The 310-1 configuration delivers DS1/DSO analysis

with DS1 scanning and automatic payload detection. It measures transmit and receive frequency, signal level, and power across digital circuits. Pulse shape analysis conforms to 1991 and 1993 ANSI and ITU-T specifications for DS3 circuits, enabling correlation of error bursts with level dropouts and frequency variations during installation. Jitter measurements extend to DS3 and SONET signals, including in-service DS3 jitter detection for SONET SPE pointer adjustment analysis. Frequency spectrum analysis operates on demodulated jitter output.

Key Features

- DS1 scan with automatic payload detection and ESF data link analysis
- Programmable loop codes and test patterns; DS1 timing slips measurement
- Fractional T1 testing for leased contiguous and non-contiguous channels
- Automatic detection of received AMI or B8ZS coded signals; transmit capability in both formats
- DS1 timing reference from SONET, DS3, DS1, or BITS access points
- DS0 voice quality verification and signaling bit analysis via drop from DS1 to OC-

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

The TTC T-BERD 310-1 is a DS1/DS0 analyzer designed for testing and troubleshooting digital communication circuits. It provides comprehensive testing capabilities for DS1 and DS0 level signals, including signal generation, measurement, and analysis. Ideal for telecom engineers and technicians.

The TTC T-BERD 310-1 DS1/DS0 Analyzer is a modular digital communications test instrument engineered for DS1 and DS0 signal generation, measurement, and analysis across telecommunications infrastructure. It addresses testing requirements in central offices, laboratories, mobile telephone switching offices, and manufacturing environments with field-upgradeable architecture supporting evolving network demands.

Typical Applications DS1/DS0 circuit troubleshooting, BITS timing isolation, fractional T1 leased channel validation, ATM traffic simulation, and SONET/SDH conformance verification.

Compatibility & Integration Modular T-BERD 310 platform architecture enables field upgrades and configuration customization. Direct access to SONET, DS3, DS1, and BITS timing references.

Key Features

DS1 scan with automatic payload detection and ESF data link analysis Programmable loop codes and test patterns; DS1 timing slips measurement Fractional T1 testing for leased contiguous and non-contiguous channels Automatic detection of received AMI or B8ZS coded signals; transmit capability in both formats DS1 timing reference from SONET, DS3, DS1, or BITS access points DS0 voice quality verification and signaling bit analysis via drop from DS1 to OC-12 access points BITS timing analysis for asynchronous and synchronous network impairment isolation ESF synchronization message decoding for BITS clock source assessment Complete pulse shape analysis for DS3 conformance verification ATM traffic simulation with advanced error insertion; ATM analysis options available SONET/SDH support for OC-3, OC-12, OC-48, and OC-48c signals with automatic conformance testing DWDM and optical media testing capabilities

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received AMI or B8ZS coded signals; transmit capability in both formats DS1 timing reference from SONET, DS3, DS1, or BITS access points DS0 voice quality verification and signaling bit analysis via drop from DS1 to OC-

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
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<https://aumictechlabs.com>

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.