

**Aumictech**

Test & measurement · bought, sold, repaired, calibrated

Data Sheet

August 12, 2026

TEKTRONIX

Tektronix BSA17500C BERTScope 17.5 Gb/s Bit Error Rate Analyzer

The Tektronix BSA17500C BERTScope is a Bit Error Rate Analyzer engineered for validation of high-speed serial communication systems operating from 1 Gb/s to 17.5 Gb/s. This instrument delivers error detection, counting, and location analysis to isolate performance limitations in transceivers, interconnects, and communication channels. With integrated jitter analysis, flexible pattern generation, and patented error location capabilities, the BSA17500C enables rapid characterization of deterministic versus random error mechanisms. Compatibility & Integration The BSA17500C supports standard and proprietary pattern libraries with flexible memory architecture, enabling adaptation to emerging serial communication standards and custom test requirements. Aumictech offers NIST-traceable calibration and repair for the Tektronix BSA17500C. All calibrations are performed to NIST-traceable standards and ship with a certificate. Turnaround is typically 1–3 business days after receipt. This Tektronix BSA17500C is listed at . Calibration is available separately from . Contact us for volume pricing or if you need multiple units.



MODEL

**Tektronix
BSA17500C**

CALIBRATION

**Available on
request**

CONDITION

Used

STATUS

In stock

RFQ / SKU

BSA17500C

LISTING

<https://aumictechlabs.com/products/bsa17500c-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 · 811210 · 541380

This unit

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Technical Specifications

Data Rate: 1 Gb/s to 17.5 Gb/s; **BER measurements to** 28.6 Gb/s **Clock Outputs:** 1–17.5 GHz main path; sub-rate outputs 0.250–3.575 GHz **Jitter Measurement:** Total Jitter (TJ), Random Jitter (RJ), and Deterministic Jitter (DJ) per industry standards **Sinusoidal Jitter Generation:** 1 kHz to 100 MHz internal frequency range **Pattern Memory:** Flexible sequencing with two-level loop nesting and up to 1 million iterations per loop

Key Features

Spread Spectrum Clocking (SSC): Generator clock path with adjustable SSC modulation for SATA, PCI Express, and SAS compliance; optional clock recovery with waveform display and measurement **Patented Error Location Analysis™:** Isolates error bursts, error-free intervals, and pattern-dependent behavior for rapid root-cause identification **Jitter Decomposition:** Optional long-pattern (PRBS-31) jitter analysis with comprehensive component breakdown **MJSQ Methodology:** One-button measurements aligned to industry jitter standards **Eye Diagram Analysis:** Integrated visualization with BER correlation; PatternVu option includes software FIR filtering for post-equalization eye views and CleanEye nondeterministic jitter removal **Mask Testing & Q-Factor:** Physical layer validation with jitter peak, BER contour, and Q-factor analysis using standard or user-defined tolerance templates

Typical Applications

Transceiver characterization and validation High-speed serial link performance assessment Channel and interconnect analysis Standards-compliant jitter and BER testing

Compatibility & Integration The BSA17500C supports standard and proprietary pattern libraries with flexible memory architecture, enabling adaptation to emerging serial communication standards and custom test requirements.

Features & description

From manufacturer datasheet

Technical Specifications

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Tektronix BSA17500C — Repair & Calibration Service

Aumictech offers NIST-traceable calibration and repair for the Tektronix BSA17500C. All calibrations are performed to NIST-traceable standards and ship with a certificate. Turnaround is typically 1–3 business days after receipt. Standard Cal Pass/fail cert + NIST sticker Cal with Data Full NIST cert with measured values Request Calibration → Request Repair Quote →

Tektronix BSA17500C price

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Tektronix BSA17500C calibration cost

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Unit notes

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Sinusoidal Jitter Generation: 1 kHz to 100 MHz internal frequency range

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Key Features

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Patented Error Location Analysis™: Isolates error bursts, error-free intervals, and pattern-dependent behavior for rapid root-cause identification

Jitter Decomposition: Optional long-pattern (PRBS-31) jitter analysis with comprehensive component breakdown

MJSQ Methodology: One-button measurements aligned to industry ji

BSA17500C Specifications

PARAMETER	VALUE
Frequency Range	1 Gb/s to 17.5 Gb/s

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

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Standard Cal Pass/fail cert + NIST sticker Cal with Data Full NIST cert with measured values

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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
<https://aumictechlabs.com>

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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.