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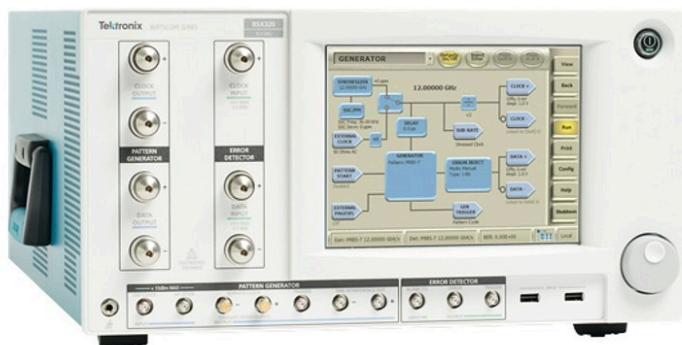
Data Sheet

August 12, 2026

TEKTRONIX

Tektronix BSX240 BERTScope Protocol-aware 24 Gb/s Bit Error Ratio Analyzer

The Tektronix BSX240 BERTScope is a protocol-aware Bit Error Ratio Analyzer designed for validation and characterization of high-speed serial communication systems. Operating from 1 Gb/s to 24 Gb/s, it delivers direct BER measurement, advanced jitter decomposition, and protocol-aware pattern generation with stimulus-response feedback. The instrument supports PCIe, SAS, USB3.1, and other serial standards, enabling rapid debugging of physical layer issues and link training across Gen4 and emerging architectures. Aumictech offers NIST-traceable calibration and repair for the Tektronix BSX240. All calibrations are performed to NIST-traceable standards and ship with a certificate. Turnaround is typically 1–3 business days after receipt. This Tektronix BSX240 is listed at . Calibration is available separately from . Contact us for volume pricing or if you need multiple units. NIST-traceable calibration for the Tektronix BSX240 is (standard — pass/fail certificate and calibration sticker) or (full measured data at every test point, reference standards documented, traceability chain included). Turnaround is 1 to 3 business days after receipt. See full calibration details →



MODEL

Tektronix BSX240

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

BSX240

LISTING

<https://aumictechlabs.com/products/bsx240-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

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Technical Specifications

- Data Rate Range:** 1 Gb/s to 24 Gb/s
- Pattern Generation and Sequencing** Flexible memory sequencing with two levels of loop nesting Up to 1 million iterations per loop Protocol-oriented and bit-oriented multi-chain pattern sequencing User-defined detector pattern matching with stimulus-response feedback Full PRBS library, standards-based patterns, and custom patterns Integrated, calibrated stress generation for receiver sensitivity and clock recovery jitter tolerance testing
- Jitter and Timing Analysis** Real-time jitter spectral analysis and Duty Cycle Distortion (DCD) measurement Comprehensive jitter decomposition: Bounded Uncorrelated Jitter (BUJ), Data Dependent Jitter (DDJ), Inter-Symbol Interference (ISI), DCD, and Sub-Rate Jitter (SRJ) BER-based, direct Total Jitter (TJ) measurement to 10^{-12} BER and beyond Correlated and non-correlated jitter component separation Jitter measurement at minimum eye opening J2 and J9 measurements for 100 GbE applications
- Protocol Support** Serial communication protocol support: PCIe, SAS, USB3.1 Flexible handshaking capabilities supporting DUT handshaking above 16 Gb/s RX test requirements and adaptive link training support Protocol-aware error detection for handshaking issue debugging
- Error Analysis** Optional Forward Error Correction (FEC) emulation

Features & description

From manufacturer datasheet

Technical Specifications

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Tektronix BSX240 — Repair & Calibration Service

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Tektronix BSX240 price

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Tektronix BSX240 calibration cost

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Unit notes

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Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

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Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.