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Data Sheet
August 12, 2026

TEKTRONIX

Tektronix PB200 packet BERT Bit Error Rate

Error detect output: RZ, TTL, 50 Ohm source. Analyzer clock disable: ECL, 50 Ohm to -2 V. Data delay output: ECL, 50 Ohm source. Common Characteristics Pattern - Controls: Start/Stop, Pause/Resume or Single Step. Save/Load: Floppy disk: 3.5 in. Internal storage: 2 Mbits. Pattern Editor Software requires PC running Windows 3.1 or above. Remote Interface - Page 2 of 3Tektronix MBD: Products > Bit Error Rate Test Set packetBERT(TM) PB200 : Character... Power consumption: 300 W maximum. Physical Characteristics Dimensionsmmin. Height203.28 Width355.614 Depth50820 Weightkglbs. Product(s) complies with IEEE Standard 488.2- 1987. Tektronix Measurement products are manufactured in ISO registered facilities. 49A-10733-3p350, 06/1997, 09/01/1999 Page 3 of 3Tektronix MBD: Products > Bit Error Rate Test Set packetBERT(TM) PB200 : Character...

No photo

MODEL Tektronix PB200	CALIBRATION N/A — off-catalog OEM datasheet
CONDITION See OEM documentation	STATUS Not listed on Aumictech inventory
RFQ / SKU PB200	LISTING Off-catalog — OEM dat asheet only

NIST-traceable cal · SAM registered · 30-day returns

NAICS 423690 · 811210 · 541380

PB200 Characteristics / Specifications

PARAMETER	VALUE
Amplitude	0.5 to 5.0 V p-p. Connector: BNC. Termination select: 50 Ohm to -2 V, +3 V, AC or GND.
Input range	ECL, TTL, PECL compatible. Threshold resolution: 10 mV step.
External clock reference input	Frequency: 10 MHz, ±100 ppm maximum.
Connector	BNC. Internal synthesized clock source: Frequency: 1 Hz to 200 MHz.
Resolution	1 Hz. Accuracy: 10 ppm. BURST clock: Programmable gap: 16 Kbits maximum, 8-Bit resolution.
Programmable word	256 Kbits maximum, 8-Bit resolution. Mark density: PRBS 2 10 1 (1/8, 1/4, 1/2, 3/4, 7/8).
Mixed mode frame	256 Kbits maximum, 8-Bit resolution. Error injection: Error: Single, Rate, External (TTL).
Field select	Overhead, payload or both. Error rates: Error rate of 10 -n , n = 3, 4, 5, 6, 7.
Source impedance	50 Ohm. Connector: BNC. Amplitude: 0.5 V to 2.0 V, 10 mV step.
Page 1 of 3Tektronix MBD	Products > Bit Error Rate Test Set packetBERT(TM) PB200 : Character... 9/15/2005http://www.tek.com/Measurement/Products/catalog/pb200/specs.html
Data delay range	±1.0 ns, 20 ps resolution. Rise/fall time: 300 ps typical, 500 ps maximum (20 to 80%).
Pattern sync output	Signal level: 250 mV p-p into 50 Ohm load.
Clock disable input	ECL, 50 Ohm to -2 V, BNC.
Data polarity	True/invert selectable. Input mode select: Single-ended or differential.
Termination select	50 Ohm to -2 V, +3 V, AC or GND.
Threshold resolution	10 mV step. Synchronization - Auto search: Clock and data threshold, clock and data timing skew, data pattern and polarity.
BER test field	Overhead, payload or both. Propagation delay: Up to 128 Kbits.
Startup delay	0 to 64 Kbits programmable. Auxiliary Signals (BNC Connectors) - Error count inhibit input: ECL, 50 Ohm to -2 V.
Error detect output	RZ, TTL, 50 Ohm source. Analyzer clock disable: ECL, 50 Ohm to -2 V.
Data delay output	ECL, 50 Ohm source. Common Characteristics Pattern - Controls: Start/Stop, Pause/Resume or Single Step.

PARAMETER	VALUE
Save/Load	Floppy disk: 3.5 in. Internal storage: 2 Mbits. Pattern Editor Software requires PC running Windows 3.1 or above.
GPIB	IEEE 488.2 compatible. RS-232C: DB9 connector. Parallel printer: DB25 connector.
Monitor	DB15, VGA Analog Output. Environmental - Operating temperature: 5 to 40°C.
Storage temperature	-10 to 60°C. Meets European Community EMC Directive 89/336/EEC for electromagnetic compatibility.
Power consumption	300 W maximum. Physical Characteristics Dimensionsmmin. Height203.28 Width355.614 Depth50820 Weightkglbs. Net13.630 Top of Page
Operating temperature	5 to 40°C.

PB200 Specifications

PARAMETER	VALUE
This product is discontinued.	View alternative products
Identical to Pattern Generator but independent.	Startup delay: 0 to 64 Kbits programmable.
Physical Characteristics	Dimensionsmmin.
Height203.28	Width355.614
Weightkglbs.	Net13.630
Specs	Product(s) complies with IEEE Standard 488.2-
manufactured in ISO registered facilities.	49A-10733-3p350, 06/1997, 09/01/1999
External clock input	Frequency: DC to 200 MHz.
Internal synthesized clock source	Frequency: 1 Hz to 200 MHz.
BURST clock	Programmable gap: 16 Kbits maximum, 8-Bit resolution.
Error injection	Error: Single, Rate, External (TTL).
Data and clock outputs	Format: NRZ.
Data and clock inputs	Clock input: External or internal from generator.

General Specifications & Supplementary Characteristics

PARAMETER	VALUE
Storage temperature	10 to 60°C. Meets European Community EMC Directive 89/336/EEC for electromagnetic compatibility.
Operating temperature	5 to 40°C.
Operating Temperature	5 to 40°C.
Power consumption	300 W maximum. Physical Characteristics Dimensionsmmin. Height203.28 Width355.614 Depth50820 Weightkglbs. Net13.630 Top of Page

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

PARAMETER	VALUE
CHARACTERISTICS	GENERATOR Clock - External clock input: Frequency: DC to 200 MHz.
Amplitude	0.5 to 5.0 V p-p. Connector: BNC. Termination select: 50 Ohm to -2 V, +3 V, AC or GND.
Input range	ECL, TTL, PECL compatible. Threshold resolution: 10 mV step.
External clock reference input	Frequency: 10 MHz, ±100 ppm maximum.
Amplitude	0.5 to 2.0 V p-p, AC coupled. Termination: 50 Ohm.
Connector	BNC. Internal synthesized clock source: Frequency: 1 Hz to 200 MHz.
Resolution	1 Hz. Accuracy: 10 ppm. BURST clock: Programmable gap: 16 Kbits maximum, 8-Bit resolution.

Pattern Generator - PRBS: 2 N - 1, N = 31, 23, 15, 11, 10, 9, 7.

Specifications

PARAMETER	VALUE
Frequency	DC to 200 MHz.
Amplitude	0.5 to 5.0 V p-p.
Connector	BNC.
Termination select	50 Ohm to -2 V, +3 V, AC or GND.
Input range	ECL, TTL, PECL compatible.
Threshold resolution	10 mV step.

Specifications

PARAMETER	VALUE
Programmable word	256 Kbits maximum, 8-Bit resolution. Mark density: PRBS 2 10 1 (1/8, 1/4, 1/2, 3/4, 7/8).
Mixed mode frame	256 Kbits maximum, 8-Bit resolution. Error injection: Error: Single, Rate, External (TTL).
Field select	Overhead, payload or both. Error rates: Error rate of 10 -n , n = 3, 4, 5, 6, 7.

Outputs - Data and clock outputs: Format: NRZ. Configurations: Differential (True/Complement). Source impedance: 50 Ohm. Connector: BNC. Amplitude: 0.5 V to 2.0 V, 10 mV step.

Offset, 50 Ohm to GND: -2.0 V to +1.8, 10 mV step (termination to 50 Ohm will increase the range for PECL). Data delay range: ±1.0 ns, 20 ps resolution. Rise/fall time: 300 ps typical, 500 ps maximum (20 to 80%). Pattern sync output: Signal level: 250 mV p-p into 50 Ohm load.

Auxiliary - Data inhibit input: ECL, 50 Ohm to -2 V, BNC.
Clock disable input: ECL, 50 Ohm to -2 V, BNC.
ANALYZER Data and Clock - Data and clock inputs: Clock input: External or internal from generator.

Specifications

PARAMETER	VALUE
Data polarity	True/invert selectable. Input mode select: Single-ended or differential.
Termination select	50 Ohm to -2 V, +3 V, AC or GND.
Input range	ECL, TTL, PECL compatible. Threshold resolution: 10 mV step.
Threshold resolution	10 mV step. Synchronization - Auto search: Clock and data threshold, clock and data timing skew, data pattern and polarity.

Measurements - Frequency: 1 Hz to 200 MHz. Bit errors: Window, Totalize, Timed Test, Simultaneous 1's, 0's and All Errors.
BER test field: Overhead, payload or both. Propagation delay: Up to 128 Kbits.
Eye-width: Up to 32 ns. Reference Data Patterns - Identical to Pattern Generator but independent.

Specifications

PARAMETER	VALUE
Startup delay	0 to 64 Kbits programmable. Auxiliary Signals (BNC Connectors) - Error count inhibit input: ECL, 50 Ohm to -2 V.
Error detect output	RZ, TTL, 50 Ohm source. Analyzer clock disable: ECL, 50 Ohm to -2 V.
Data delay output	ECL, 50 Ohm source. Common Characteristics Pattern - Controls: Start/Stop, Pause/Resume or Single Step.
Save/Load	Floppy disk: 3.5 in. Internal storage: 2 Mbits. Pattern Editor Software requires PC running Windows 3.1 or above.

Specifications

PARAMETER	VALUE
GPIB	IEEE 488.2 compatible. RS-232C: DB9 connector. Parallel printer: DB25 connector.
Monitor	DB15, VGA Analog Output. Environmental - Operating temperature: 5 to 40°C.
Storage temperature	-10 to 60°C. Meets European Community EMC Directive 89/336/EEC for electromagnetic compatibility.

Safety - UL1244, CSA231, EN61010-1, IEC61010-1. Power - AC line: 100 to 120 V AC, 200 to 240 V AC.
Power consumption: 300 W maximum. Physical Characteristics Dimensionsmmin. Height203.28 Width355.614

Depth50820 Weightkglbs.
Net13.630

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Features Specs

Product(s) complies with IEEE Standard 488.2- 1987.

Tektronix Measurement products are manufactured in ISO registered facilities.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: N/A — off-catalog OEM datasheet. See OEM documentation · Not listed on Aumictech inventory.

Request a quote: **sales@aumictech.com** · +1 (484) 841-9341

Aumictech
788 S 500 E, Pleasant Grove, UT 84062
sales@aumictech.com · +1 (484) 841-9341
https://aumictechlabs.com

NIST-traceable calibration · SAM registered · 30-day returns
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Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.