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Data Sheet
August 12, 2026

TELEDYNE TEST TOOLS

Teledyne Test Tools T3DSO2000-LS MSO Logic Probe Set

The Teledyne Test Tools T3DSO2000-LS is a logic probe set designed for use with Teledyne Test Tools T3DSO2000 series mixed-signal oscilloscopes. This set allows users to analyze digital signals with their T3DSO2000 series oscilloscope. It enables the capture and analysis of digital signals, providing valuable insights into the behavior of digital circuits and systems.

MODEL Teledyne Test Tools T3DSO2000-LS	CALIBRATION Available on request
CONDITION Used	STATUS In stock
RFQ / SKU T3DSO2000-LS	LISTING https://aumictechlabs.com/products/t3dso2000-ls-2/

NIST-traceable cal · SAM registered · 30-day returns · NAICS 423690 · 811210 · 541380

This unit

The Teledyne Test Tools T3DSO2000-LS MSO Logic Probe Set extends mixed-signal measurement capability to compatible T3DSO2000A and T3DSO3000HD series oscilloscopes. This 16-channel logic probe set captures and analyzes digital signals alongside analog waveforms, enabling comprehensive debugging of complex digital systems in embedded platforms and mixed-signal environments.Technical Specifications Digital Channels: 16 Sampling Rate: 500 MSa/s Memory Depth: 14 MptsKey Features The 500 MSa/s sampling rate captures fast-changing digital signal transitions with sufficient temporal resolution for timing-critical applications. The 14 Mpts memory depth records extended digital sequences, supporting in-depth

analysis of protocol sequences and transient behavior across all 16 channels simultaneously. Mixed-signal triggering allows engineers to establish trigger conditions across both analog and digital channels, correlating analog performance with digital state changes. Advanced protocol decoding - including I2C, SPI, UART, CAN, and LIN - leverages the parent oscilloscope's triggering and decoding engine for serial bus analysis.Typical Applications This probe set serves embedded systems development, digital communication protocol verification, timing validation, and logic circuit analysis where analog signal behavior must be examined in parallel with digital state and timing. Engineers in board-level debugging and system integration benefit from simultaneous observation of power supply behavior, clock signals, and data lines.Compatibility & Integration The T3DSO2000-LS requires the T3DSO2000-MSO option installed on T3DSO2000A series oscilloscopes. T3DSO3000HD series oscilloscopes with MSO configuration include the 16-channel logic probe as standard. Connection to compatible oscilloscopes is made via interface leads specific to each parent instrument model.

Features & description

From manufacturer datasheet

Features

• Digital channels: 16 (groups D0–D7 and D8–D15 with independent thresholds) • Max sampling rate: 500 MSa/s • Memory depth: 14 Mpts class (T3DSO2000A listings) / 50 Mpts/ch (T3DSO2000HD datasheet digital section) • Minimum detectable pulse width: 3.3 ns (HD datasheet) • Level range / custom threshold: −10 V to +10 V • Logic types: TTL, CMOS, LVCMOS 3.3 V, LVCMOS 2.5 V, Custom • Skew: D0–D15 ±1 sampling interval; digital-to-analog ±(1 sampling interval + 1 ns) • Compatibility: T3DSO2000A (needs MSO option), T3DSO3000HD / T3DSO2000HD class

T3DSO2000LS Characteristics / Specifications

PARAMETER	VALUE
Model	T3DSO2000-LS
Manufacturer	Teledyne Test Tools (Teledyne LeCroy)
Instrument Type	MSO 16-Channel Logic Probe Set
Alias	T3DSO2000-LS; T3 MSO logic probe Digital Acquisition
Channels	16
Sample rate	500 MSa/s
Memory	14 Mpts (2000A class listing) / 50 Mpts/ch (2000HD datasheet)
Min pulse width	3.3 ns
Thresholds	two groups; TTL/CMOS/LVCMOS3.3/LVCMOS2.5/Custom (−10 to +10 V) Notes Specs from Teledyne T3DSO2000HD datasheet digital section and T3DSO2000-LS product listings. Memory depth depends on host scope series–confirm against the oscilloscope in use. Probe alone does not enable MSO on 2000A without the software option.
Digital channels	16 (groups D0–D7 and D8–D15 with independent thresholds)
Max sampling rate	500 MSa/s
Memory depth	14 Mpts class (T3DSO2000A listings) / 50 Mpts/ch (T3DSO2000HD datasheet digital section)
Minimum detectable pulse width	3.3 ns (HD datasheet)
Level range / custom threshold	−10 V to +10 V
Logic types	TTL, CMOS, LVCMOS 3.3 V, LVCMOS 2.5 V, Custom
Skew	D0–D15 ±1 sampling interval; digital-to-analog ±(1 sampling interval + 1 ns)
Compatibility	T3DSO2000A (needs MSO option), T3DSO3000HD / T3DSO2000HD class

T3DSO2000LS Specifications

PARAMETER	VALUE
Applications	Mixed-signal oscilloscope debug; parallel bus capture; multi-family logic threshold monitoring (two independent 8-bit groups).
Key Features	- Digital channels: 16 (groups D0–D7 and D8–D15 with independent thresholds)
Technical Specifications	Model: T3DSO2000-LS
Digital Acquisition	Channels: 16

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications

Specifications

PARAMETER	VALUE
Model	T3DSO2000-LS
Manufacturer	Teledyne Test Tools (Teledyne LeCroy)
Instrument Type	MSO 16-Channel Logic Probe Set
Alias	T3DSO2000-LS; T3 MSO logic probe

Digital Acquisition

Specifications

PARAMETER	VALUE
Channels	16
Sample rate	500 MSa/s
Memory	14 Mpts (2000A class listing) / 50 Mpts/ch (2000HD datasheet)
Min pulse width	3.3 ns
Thresholds	two groups; TTL/CMOS/LVCMOS3.3/LVCMOS2.5/Custom (−10 to +10 V)

Notes
Specs from Teledyne T3DSO2000HD datasheet digital section and T3DSO2000-LS product listings. Memory depth depends on host scope series—confirm against the oscilloscope in use. Probe alone does not enable MSO on 2000A without the software option.

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: **sales@aumictech.com** · +1 (484) 841-9341

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Generated August 12, 2026