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Data Sheet

August 12, 2026

YOKOGAWA

Yokogawa SB5710 Serial Data Analyzer

Yokogawa SB5710 Vehicle Serial Bus Analyzer (SB5000 Series) The Yokogawa SB5710 (order class 701361) is the 4-analog-channel + 32-bit logic model of the discontinued SB5000 Vehicle Serial Bus Analyzer family. It combines high-speed oscilloscope acquisition (up to 5 GS/s, 1 GHz bandwidth, 6.25 Mpts/ch) with dedicated FlexRay/CAN/LIN/UART/I²C/SPI trigger, decode, and analysis for automotive ECU and in-vehicle network development. Discontinued 2013-03-13 (Yokogawa replacement class: DLM5000). FlexRay physical-layer and protocol debug; CAN/LIN subnetwork analysis; UART/I²C/SPI ECU internal buses; parallel logic (up to 32-bit) with analog correlation; FlexRay eye-diagram and SI voting analysis; history-memory capture of intermittent bus faults. The Yokogawa SB5710 (order class 701361) is the 4-analog-channel + 32-bit logic model of the discontinued SB5000 Vehicle Serial Bus Analyzer family. It combines high-speed oscilloscope acquisition (up to 5 GS/s, 1 GHz bandwidth, 6.25 Mpts/ch) with dedicated FlexRay/CAN/LIN/UART/I²C/SPI trigger, decode, and analysis for automotive ECU and in-vehicle network development. Discontinued 2013-03-13 (Yokogawa replacement class: DLM5000). FlexRay physical-layer and protocol debug; CAN/LIN subnetwork analysis; UART/I²C/SPI ECU internal buses; parallel logic (up to 32-bit) with analog correlation; FlexRay eye-diagram and SI voting analysis; history-me



MODEL

Yokogawa SB5710

CALIBRATION

Available on request

CONDITION

Used

STATUS

In stock

RFQ / SKU

SB5710

LISTING

<https://aumictechlabs.com/products/sb5710-2/>

NIST-traceable cal

SAM registered

30-day returns

NAICS 423690 • 811210 • 541380

This unit

The Yokogawa SB5710 Serial Data Analyzer captures, displays, and analyzes serial communication data across automotive and complex electronic systems. It combines 4 analog channels with 32-bit logic analysis, 1.0 GHz bandwidth, and up to 2.5 GS/s sampling to decode FlexRay, CAN, LIN, UART, I²C, and SPI protocols. The 6.25 MW memory supports segmented recording up to 2000 divisions, enabling extended capture sessions and detailed waveform inspection.

Technical Specifications

Model: SB5710 (701361) Bandwidth: 1.0 GHz Sample Rate: Up to 2.5 GS/s Memory Depth: 6.25 MW (segmentable to 2000 divisions) Analog Channels: 4 Logic Channels: 32-bit Supported Protocols: FlexRay, CAN, LIN, UART, I²C, SPI Interfaces: IEEE 488.2 (GPIB), USB, Ethernet

Key Features

- Protocol analysis with symbolic triggering and database-driven decoding (DBC for CAN, FIBEX for FlexRay)
- FlexRay physical layer conformance testing including eye-diagram analysis, mask testing, and electrical parameter characterization
- CAN bus symbolic analysis with trend display and error triggering
- Parallel bus measurement and analysis up to 32 bits wide
- Advanced triggering across multiple bus types and analog channels with dedicated error triggers
- Electrical characteristics testing for delay times, rise/fall times, and driver parameters
- History replay of captured data
- Auto-setup and protocol-optimized user interface menus
- Optional power supply analysis capability

Typical Applications

Automotive embedded systems validation, FlexRay physical layer conformance verification, multi-protocol bus debugging, microprocessor parallel interface analysis, and serial communication troubleshooting in high-speed digital designs.

Compatibility & Integration

Remote control and integration via GPIB, USB, and Ethernet connectivity enables automated test sequences and data export to analysis tools.

Features & description

From manufacturer datasheet

Features

- Analog channels: 4
- Analog bandwidth: 1 GHz
- Maximum sample rate: 5 GS/s (2.5 GS/s per channel)
- Memory: 6.25 MW (Mpts) per channel
- Logic: 32-bit (requires optional logic probes 701980 or 701981)
- Serial buses: FlexRay, CAN, LIN, UART, I²C, SPI – trigger, analysis, decode
- FlexRay eye-diagram mask/parameter tests; FlexRay SI Voting Procedures Analysis (firmware ≥ 4.42)
- CAN/FlexRay symbolic triggering/analysis/trend (DBC / FIBEX databases; Symbol Editor PC software)
- Dual-bus simultaneous CAN analysis (e.g. Hi-Speed + Low-Speed)
- History Memory: up to 2000 segments; History Replay
- Serial-bus dedicated Auto Setup and SERIAL BUS SETUP menu
- Connectivity: USB-PC; Video OUT; Trigger I/O; PC Card slot (GPIB via NI PCMCIA-GPIB); optional probe power; optional Ethernet 100BaseTX/10BaseT; optional LXI ClassCEthernet (/C12)
- GO/NO-GO I/O (TTL) for GO/NO-GO or mask-test results

SB5710 Characteristics / Specifications

PARAMETER	VALUE
Model	SB5710
Order / catalog class	701361
Manufacturer	Yokogawa
Instrument Type	Vehicle Serial Bus Analyzer / Mixed-signal oscilloscope-class analyzer
Family	SB5000 Series
Alias	SB5710; Yokogawa SB5710; SB5000 SB5710 Acquisition (SB5710 model row)
Analog channels	4
Bandwidth	1 GHz
Max sample rate	5 GS/s (2.5 GS/s/ch)
Record length	6.25 MW (Mpts) / ch
Logic channels	32 bits (probes 701980 / 701981 required) Sibling model (for contrast)
SB53	4 ch 1 GHz + Logic 8 bits; Max 5 GS/s (2.5 GS/s/ch); 6.25 MW/ch Serial Bus Analysis
FlexRay	frame/ID/cycle/data triggers; CRC/BSS/FES error triggers; list/decode; FIBEX symbolic; eye-diagram; SI voting (FW ≥ 4.42); bus-driver electrical characteristic parameters
CAN	SOF/ID/Data/Remote/Error triggers; multi-ID/Data OR combinations; list/decode; DBC symbolic; dual-bus simultaneous analysis
LIN	Break+Synch, ID/Data, error triggers; LIN 1.3 and 2.0 simultaneous decode
UART	8-bit $\pm$ parity / 7-bit+parity; parity/framing error triggers
History Memory segments	up to 2000
History Replay	play/stop/fast-forward/rewind of captured segments Interfaces / Options USB PC control/data upload Video OUT External trigger IN/OUT PC Card slot (GPIO with NI PCMCIA-GPIB)
Logic probe inputs	one or four 8-bit probes (701980/701981)
Rack mount kit class	701983 (EIA/JIS) Lifecycle
Discontinued	2013-03-13 (Yokogawa discontinued notice)

PARAMETER	VALUE
Suggested replacement class	DLM5000 Mixed Signal Oscilloscope Notes Numeric specs from Yokogawa SB5000 product documentation model table for SB5710. Confirm installed options (Ethernet, probe power, LXI /C12), firmware version, and presence of logic probes on the unit. Do not confuse SB5710 (32-bit logic) with SB5310 (8-bit logic).
Analog bandwidth	1 GHz
Maximum sample rate	5 GS/s (2.5 GS/s per channel)
Memory	6.25 MW (Mpts) per channel
Logic	32-bit (requires optional logic probes 701980 or 701981)
Serial buses	FlexRay, CAN, LIN, UART, I ² C, SPI – trigger, analysis, decode
History Memory	up to 2000 segments; History Replay
Connectivity	USB-PC; Video OUT; Trigger I/O; PC Card slot (GPIB via NI PCMCIA-GPIB); optional probe power; optional Ethernet 100BaseTX/10BaseT; optional LXI ClassCEthernet (/C12)

Full OEM specifications

Complete technical content extracted from the manufacturer datasheet. Sparse OEM tables are reconstructed as tables below; other text is preserved as paragraphs.

Specifications	
Specifications	
PARAMETER	VALUE
Model	SB5710
Order / catalog class	701361
Manufacturer	Yokogawa
Instrument Type	Vehicle Serial Bus Analyzer / Mixed-signal oscilloscope-class analyzer
Family	SB5000 Series
Alias	SB5710; Yokogawa SB5710; SB5000 SB5710
Acquisition (SB5710 model row)	
Specifications	
PARAMETER	VALUE
Analog channels	4
Bandwidth	1 GHz
Max sample rate	5 GS/s (2.5 GS/s/ch)
Record length	6.25 MW (Mpts) / ch
Logic channels	32 bits (probes 701980 / 701981 required)
Sibling model (for contrast)	
SB5310: 4 ch 1 GHz + Logic 8 bits; Max 5 GS/s (2.5 GS/s/ch); 6.25 MW/ch	
Serial Bus Analysis	
FlexRay: frame/ID/cycle/data triggers; CRC/BSS/FES error triggers; list/decode; FIBEX symbolic; eye-diagram; SI voting (FW ≥4.42); bus-driver electrical characteristic parameters	
Specifications	
PARAMETER	VALUE
CAN	SOF/ID/Data/Remote/Error triggers; multi-ID/Data OR combinations; list/decode; DBC symbolic; dual-bus simultaneous analysis
LIN	Break+Synch, ID/Data, error triggers; LIN 1.3 and 2.0 simultaneous decode
UART	8-bit ± parity / 7-bit+parity; parity/framing error triggers
I ² C and SPI: trigger and analysis standard; SPI without CS assignment (FW ≥4.42)	

History / Utility

History Memory segments: up to 2000

History Replay: play/stop/fast-forward/rewind of captured segments

Interfaces / Options

USB PC control/data upload

Video OUT

External trigger IN/OUT

PC Card slot (GPIB with NI PCMCIA-GPIB)

Logic probe inputs: one or four 8-bit probes (701980/701981)

Optional: probe power; Ethernet; LXI-compliant Ethernet /C12

Rack mount kit class: 701983 (EIA/JIS)

Lifecycle

Discontinued: 2013-03-13 (Yokogawa discontinued notice)

Suggested replacement class: DLM5000 Mixed Signal Oscilloscope

Notes

Numeric specs from Yokogawa SB5000 product documentation model table for SB5710. Confirm installed options (Ethernet, probe power, LXI /C12), firmware version, and presence of logic probes on the unit. Do not confuse SB5710 (32-bit logic) with SB5310 (8-bit logic).

Available from Aumictech

Used and refurbished unit evaluated in-house. Calibration: Available on request. Used · In stock.

Request a quote: sales@aumictech.com · +1 (484) 841-9341

Aumictech

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Generated August 12, 2026

Technical content from manufacturer specifications. Unit photo/stock from Aumictech listing when available. Specifications subject to OEM documentation and unit condition.